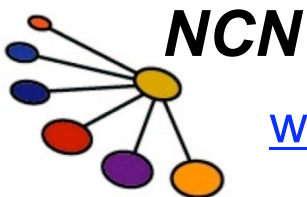


**2009 NCN@Purdue-Intel Summer School**  
**Notes on Percolation and Reliability Theory**

**Lecture 4**  
**Stick Percolation and**  
**Nanonet Electronics**

**Muhammad A. Alam**  
Electrical and Computer Engineering  
Purdue University  
West Lafayette, IN USA



[www.nanohub.org](http://www.nanohub.org)

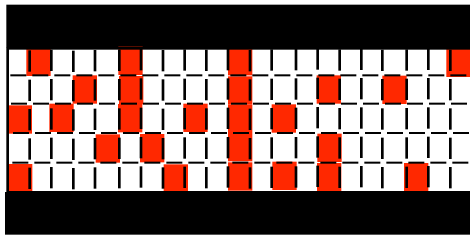
**PURDUE**  
UNIVERSITY

# outline lecture 4

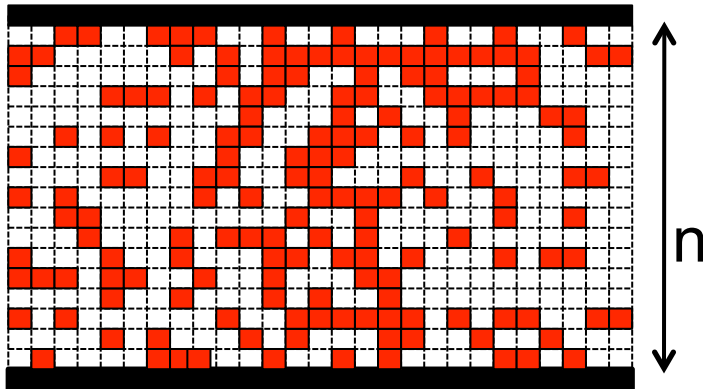
- 1) Stick percolation and nanonet transistors**
- 2) Short channel nanonet transistors
- 3) Long channel nanonet transistors
- 4) Transistors at high voltages
- 5) Conclusions

# lecture 3 vs. lecture 4

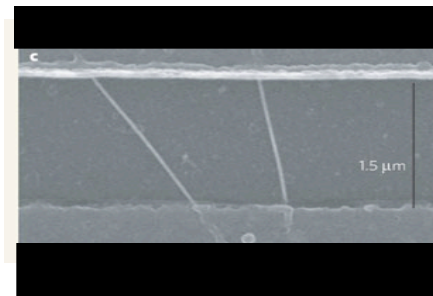
$$G \sim \sigma_{row} p^L \frac{W}{L}$$



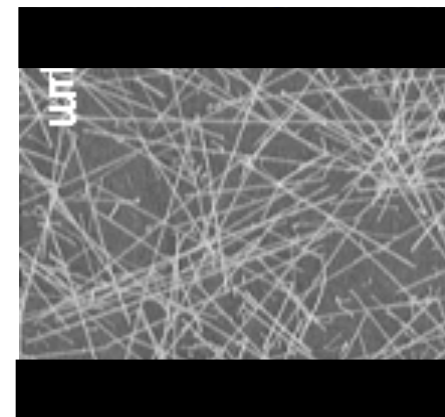
$$G \sim \sigma_{row} \frac{W}{L^\alpha}$$



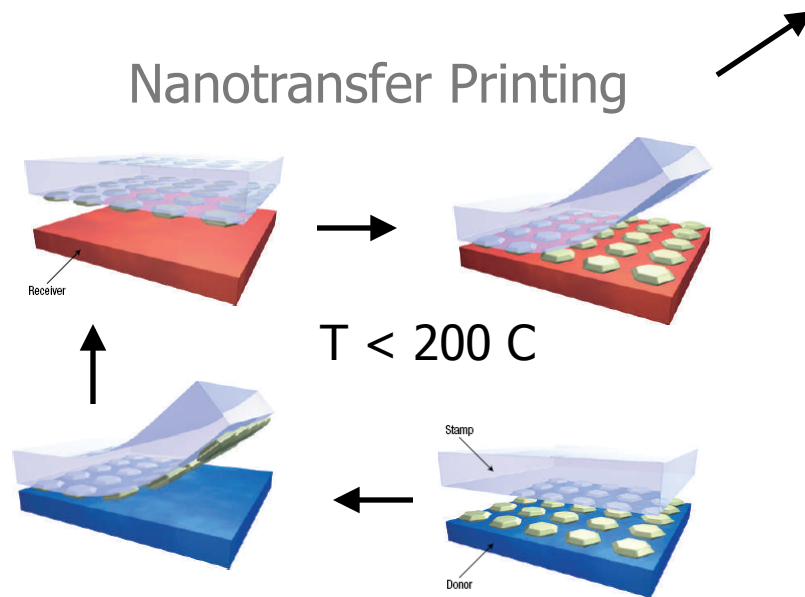
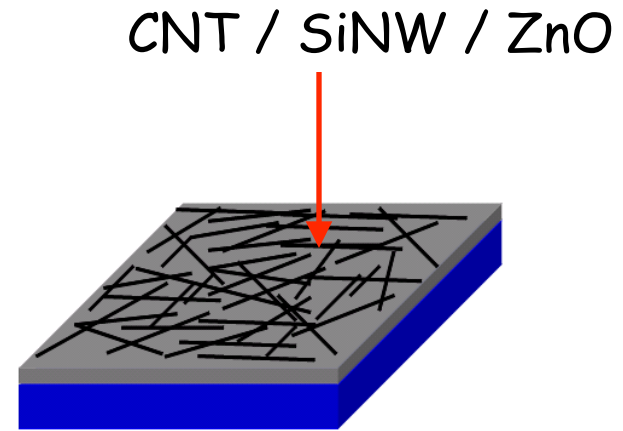
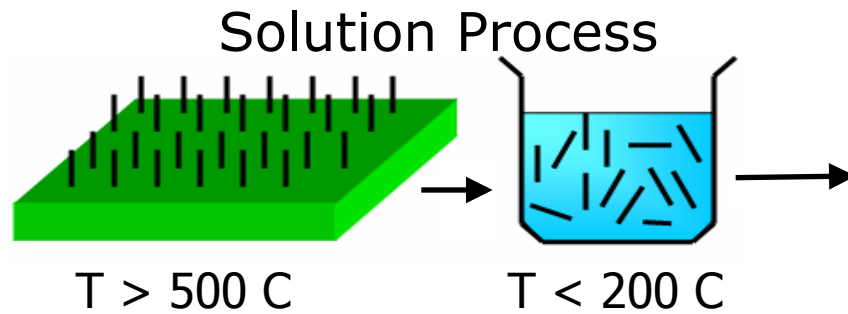
G ?



G ?



# how to make nanonet transistors ?

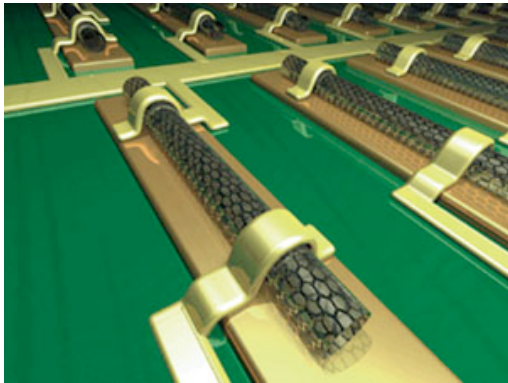


## Advantages

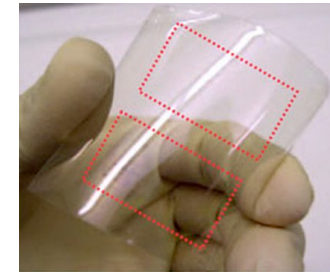
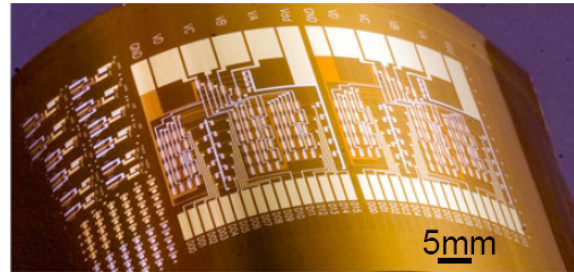
- ◆ Highly crystalline CNT / SiNW by high temperature process
- ◆ Plastic, glass or organic substrate :  
Low temperature final step
- ◆ Transparent and conducting

# why do we make nanonet transistors ?

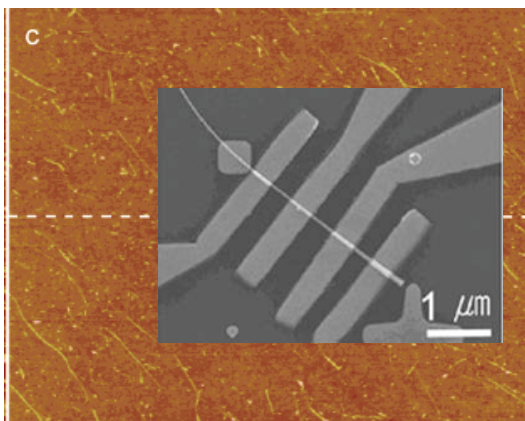
Ideally ...



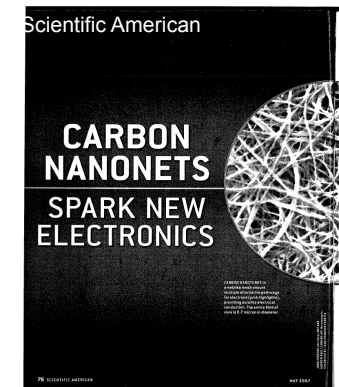
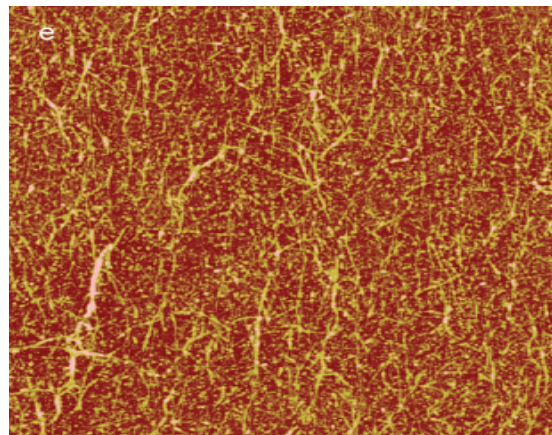
flexible electronics ?



In practice ...



...better if ...

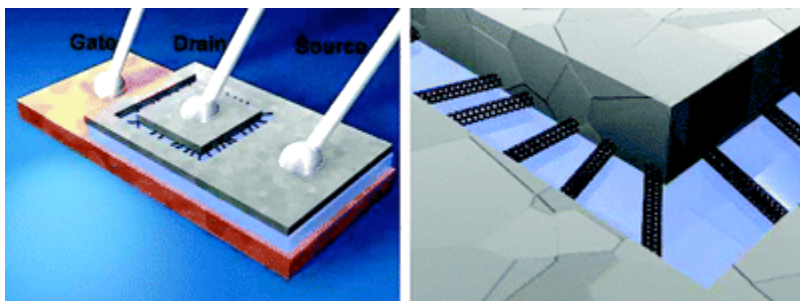
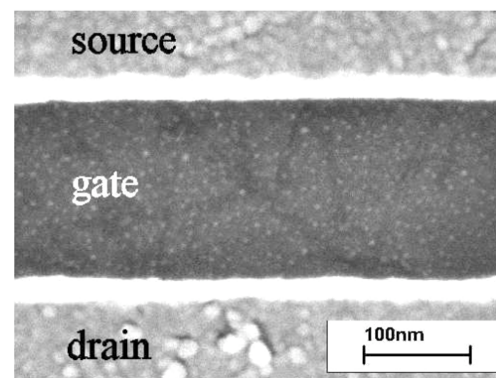
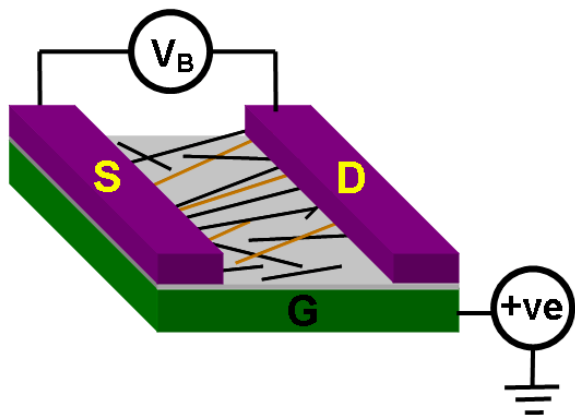


... lot of interest  
in this topic.

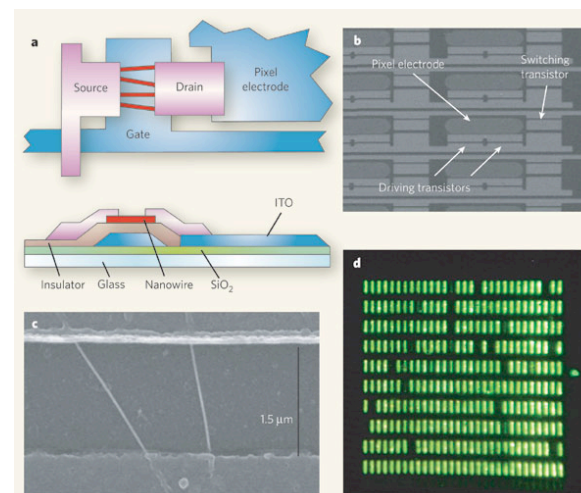
## outline lecture 4

- 1) Stick percolation and nanonet transistors
- 2) Short channel nanonet transistors**
- 3) Long channel nanonet transistors
- 4) Transistors at high voltages
- 5) Conclusions

# short channel nanonet transistors



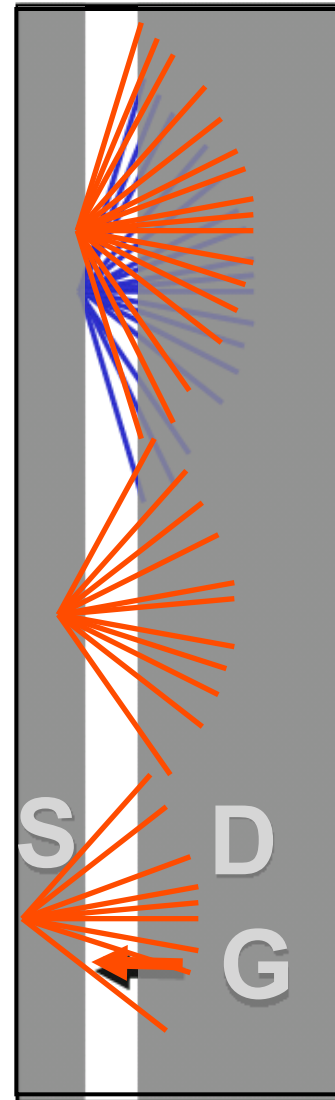
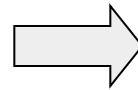
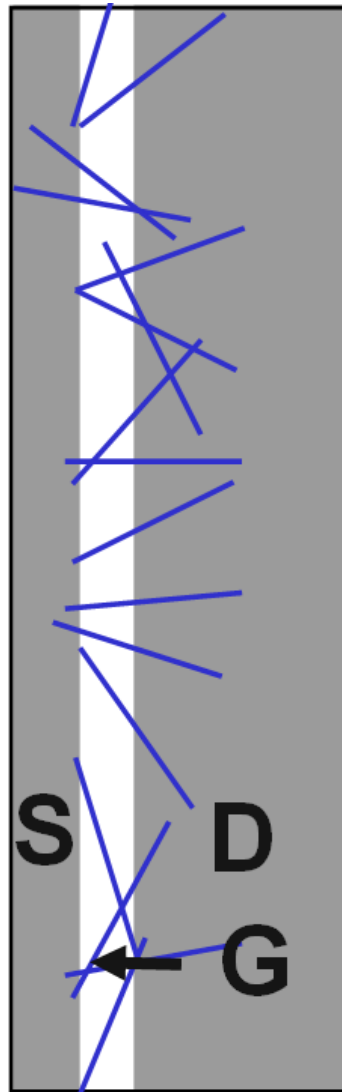
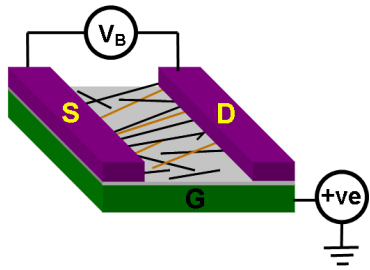
Seidel, *NanoLetters*, 2004.



Janes, *Nature Nanotech*, 2008.



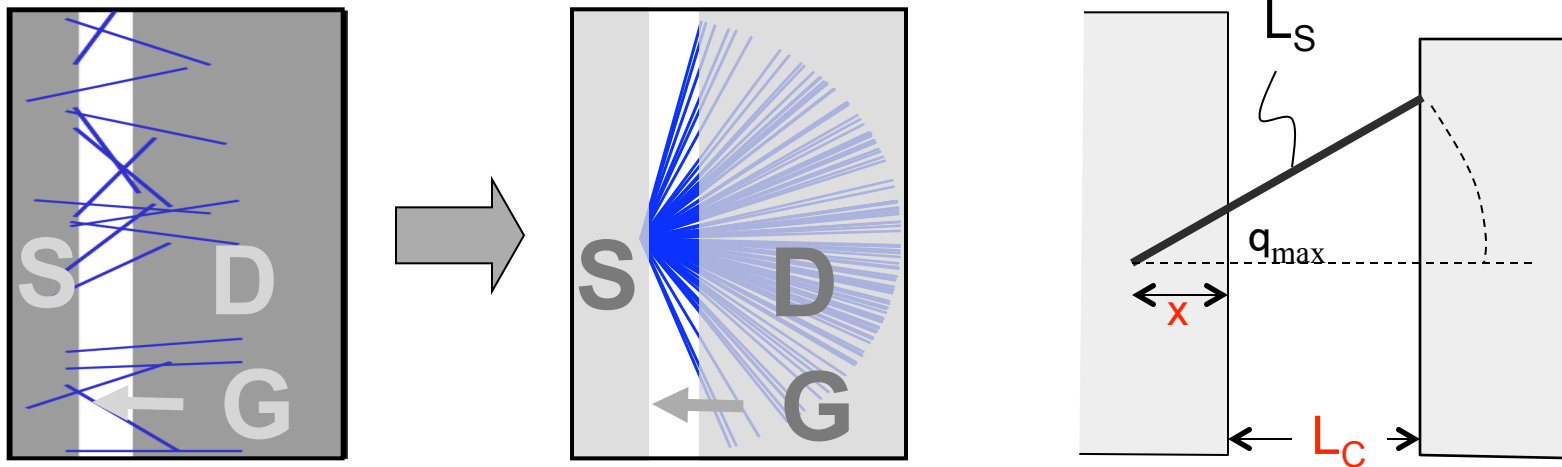
# short channel stick percolation



**Fan Diagram:**  
Collect all the  
sticks to one point



# number of bridging sticks



$$N_s = \int_0^{L_S - L_C} D_x dx \frac{\theta_{\max}(x)}{\pi/2} = \int_0^{L_S - L_C} \frac{2}{\pi} D_x dx \cos^{-1} \frac{x + L_C}{L_S}$$

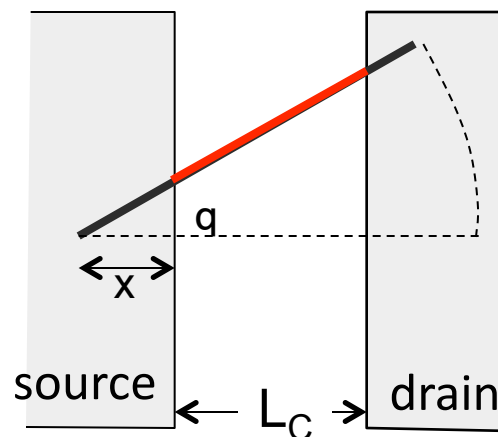
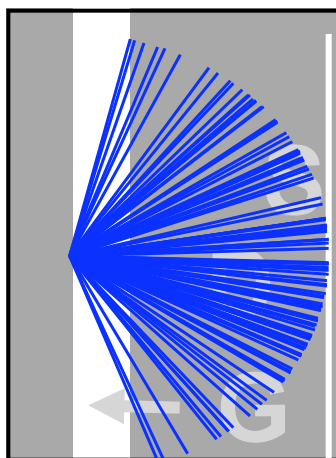
$$\cos \theta_{\max} = \frac{L_C + x}{L_S}$$

$$N_s = \frac{2D_C L_S}{\pi} \left[ \sqrt{1 - R^2} - R \cos^{-1} R \right]$$

$$R = \frac{L_C}{L_S}$$

- Generalized Buffon Needle Problem
- If needle is curved, use chord length

# I-V characteristics: ballistic transport



$$I_D^{(i)} = \frac{q}{\pi \hbar} \int_{E_b(V_G)}^{\infty} dE [f_0(E_{FS}) - f_0(E_{FD})] \equiv f(V_G, V_D)$$

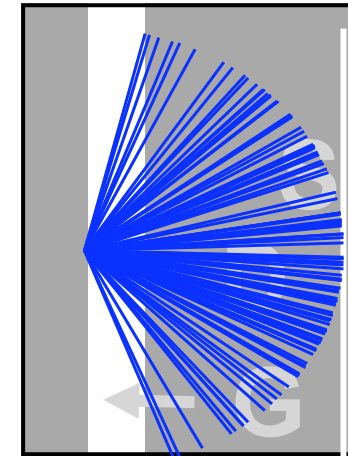
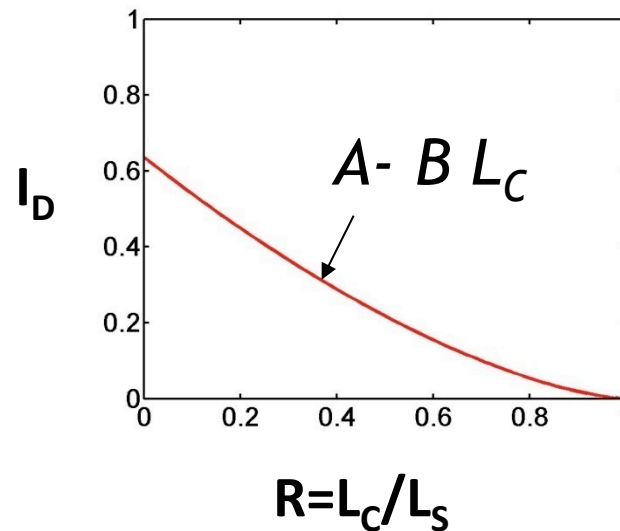
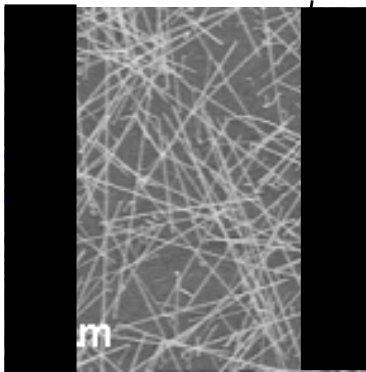
$$I_D = \sum_1^N f = \int_0^{L_S - L_C} D_x dx \frac{2}{\pi} \theta_{\max} f = f \frac{2D_C L_S}{\pi} \left[ \sqrt{1 - \left(\frac{L_C}{L_S}\right)^2} - \frac{L_C}{L_S} \cos^{-1} \frac{L_C}{L_S} \right]$$

Electrical Part

Geometrical Part

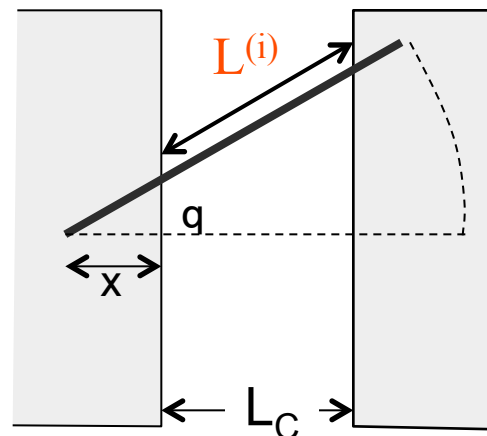
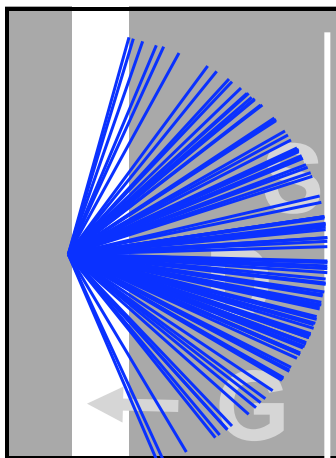
# ballistic transport and length-scaling ....

$$I_D = f(V_G, V_D) \times \frac{2D_C L_S}{\pi} \left[ \sqrt{1 - \left(\frac{L_C}{L_S}\right)^2} - \frac{L_C}{L_S} \cos^{-1} \frac{L_C}{L_S} \right]$$



length dependence even for ballistic transport,  
nothing to do it with mobility !

# I-V characteristics: with scattering



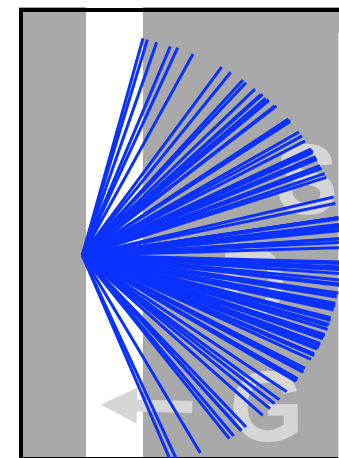
$$I_D^{(i)} = \frac{q}{\pi \hbar} \int_{E_b(V_G)}^{\infty} dE \left[ \frac{\lambda}{\lambda + L^{(i)}} \right] [f_0(E_{FS}) - f_0(E_{FD})]$$

$$\approx \frac{\langle \lambda \rangle}{\langle \lambda \rangle + L^{(i)}} \frac{q}{\pi \hbar} \int_0^{\infty} dE [f_0(E_{FS}) - f_0(E_{FD})]$$

$$I_D = \sum_1^N \frac{\langle \lambda \rangle}{\langle \lambda \rangle + L^{(i)}} \times f = f \times \sum_1^N \frac{\langle \lambda \rangle}{\langle \lambda \rangle + L_C / \cos(\theta)}$$

# I-V characteristics: with scattering

$$I_D = f \times \sum_1^N \frac{\langle \lambda \rangle}{\langle \lambda \rangle + L_C / \cos(\theta)}$$



$$\frac{I_D}{f} = \frac{2D_C}{\pi b^2} \left[ b g_B \left( \frac{L_C}{L_S} \right) - \cos^{-1} \frac{L_C}{L_S} + \frac{2(bL_C / L_S + 1)}{\sqrt{b^2 - 1}} \tanh^{-1} \frac{(b-1) \tan(\theta_S / 2)}{\sqrt{b^2 - 1}} \right]$$

$$I_D = \underbrace{f(V_G, V_D)}_{\text{Electrical Part}} \underbrace{\xi \left( \frac{L_C}{L_S}, D_C L_S \right)}_{\text{Geometrical Part}} \sim \sigma_0 \frac{W}{L^\alpha}$$

Experimental verification of fan-diagram in the appendix ...

## .... a remarkable formula

Scaling variables ...

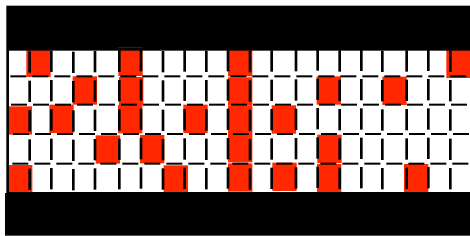
$$I_T = f(V_D, V_G) \times \xi \left( \frac{L_C}{L_S}, D_C L_S^2 \right)$$

↑  
Classical Transistor  
Theory (1950s -- )

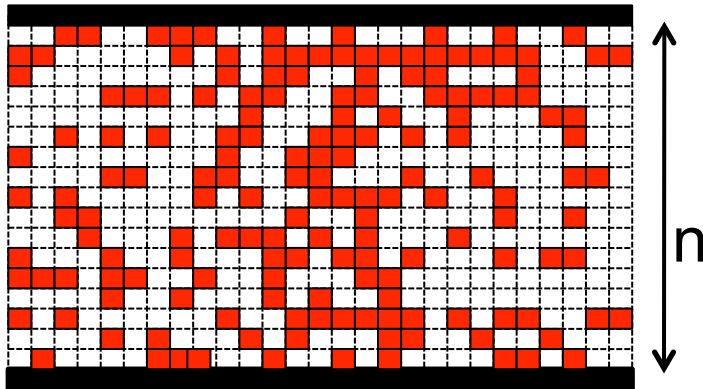
↑  
Classical percolation  
Theory (1970s -- )

# lecture 3 vs. lecture 4

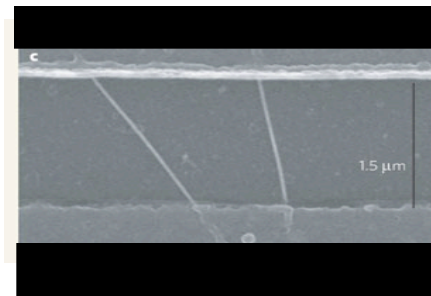
$$G \sim \sigma_{row} p^L \frac{W}{L}$$



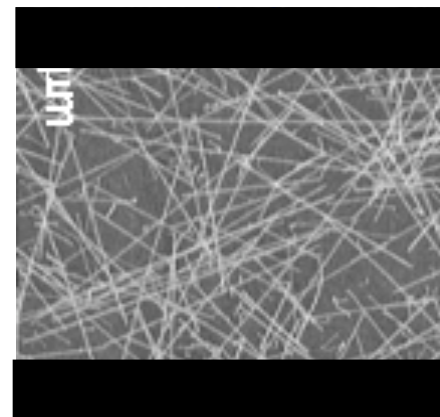
$$G \sim \sigma_{row} \frac{W}{L^\alpha}$$



$$G = \frac{2q^2}{\pi^2 \hbar} D_C L_S \left[ \sqrt{1 - \left( \frac{L_C}{L_S} \right)^2} - \frac{L_C}{L_S} \cos^{-1} \frac{L_C}{L_S} \right]$$



**G ?**



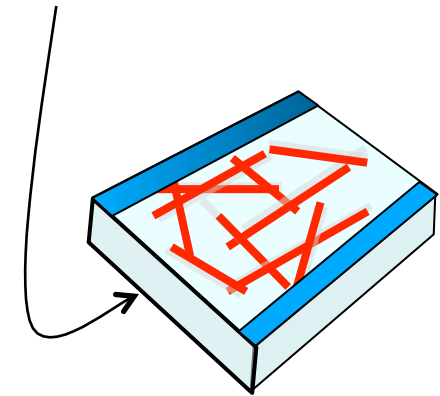
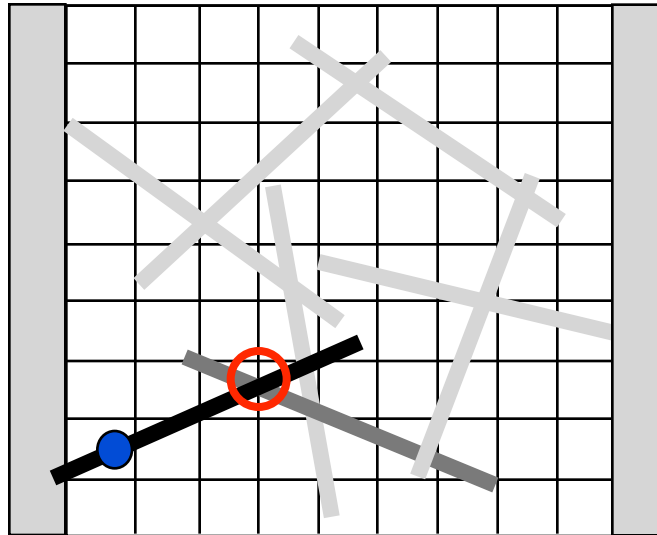


## outline lecture 4

- 1) Stick percolation and nanonet transistors
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# long channel nanonets: numerical model

$$\sum_i \nabla^2 \Phi_i + \frac{\rho_i}{\epsilon} = \frac{d^2 \Phi_i}{ds^2} + \frac{\rho_i}{\epsilon} + \sum_{j \neq i} \frac{\Phi_j - \Phi_i}{\lambda_{ij}^2} - \frac{\Phi_i - V_G}{\lambda^2} = 0$$

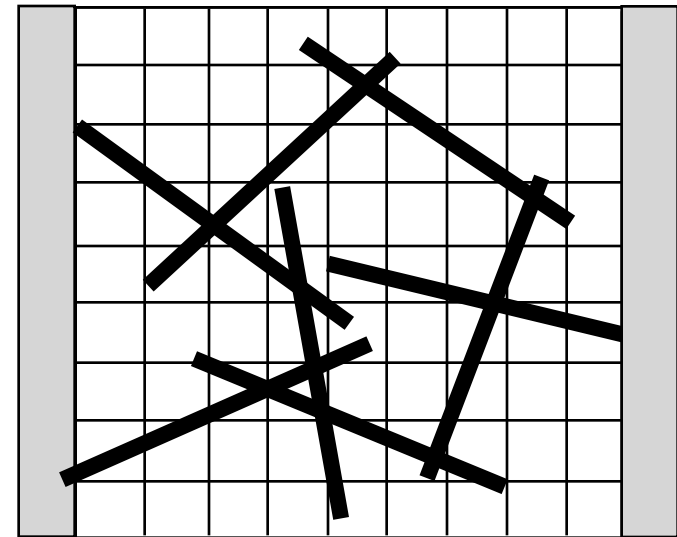


$$J_{n,i} = qn\mu E - qD \frac{dn}{ds}$$

$$\sum_i \frac{dJ_{n,i}}{ds} - \sum_{i \neq j} c_{ij}^n (n_i - n_j) = 0$$

# don't try this at home: use nanohub

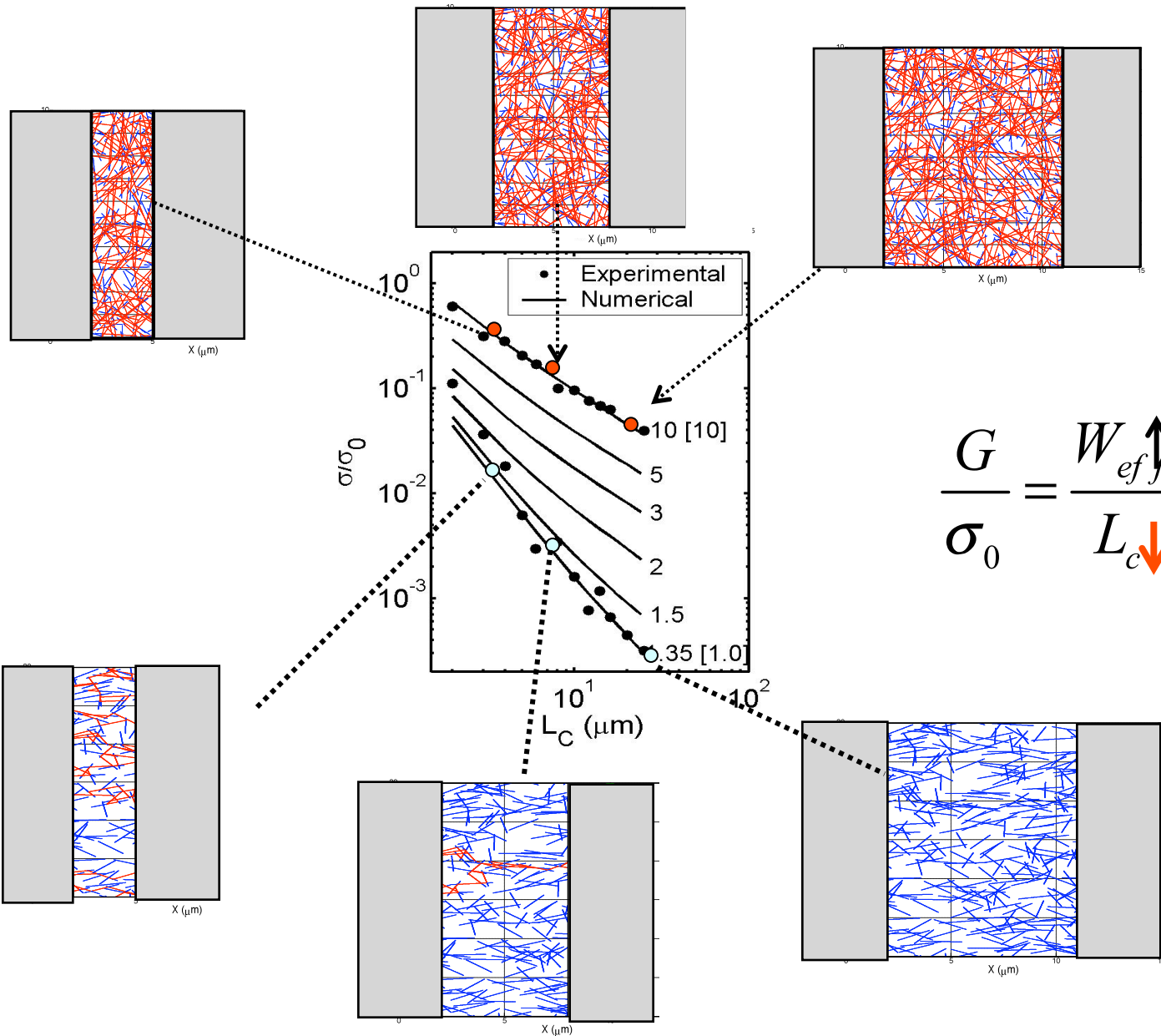
- ▶ Analytical solution not possible
- ▶ Self consistent numerical DD-Poisson solver
- ▶ Solve for hundreds of configuration
- ▶ Solve for various biases



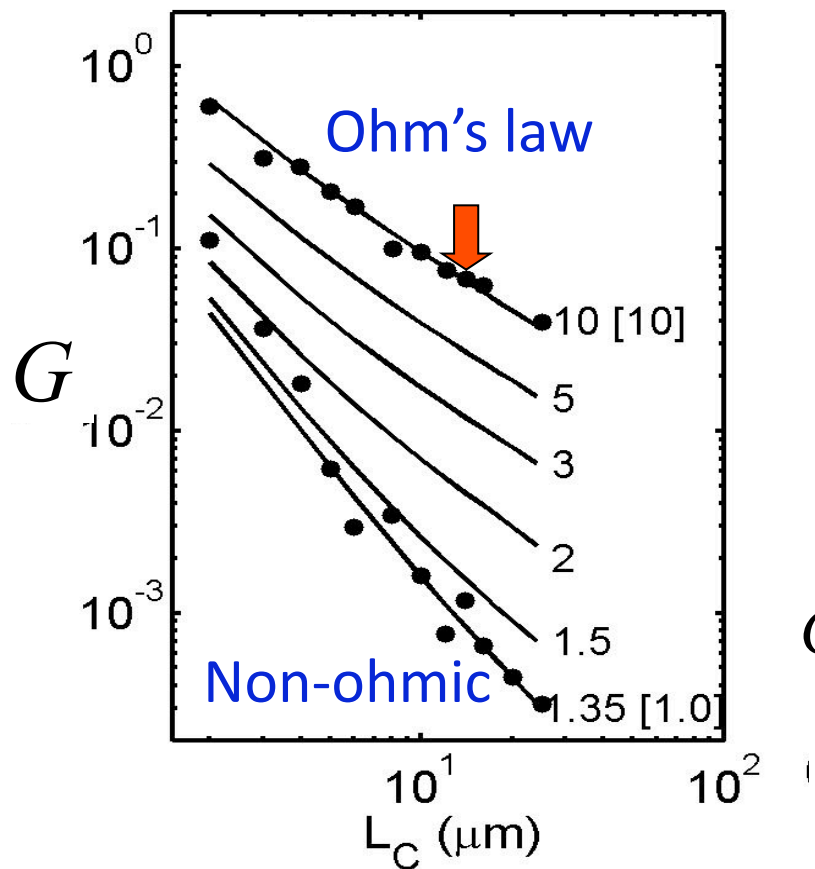
Simulator at [www.nanohub.org](http://www.nanohub.org) as 'NanoNET'

# the end of Ohm's law ...

$$I_D = f(V_D, V_G) \times \xi \left( \frac{L_S}{L_C}, DL_S^2 \right)$$



## geometrical scaling function



$$I_D = f(V_D, V_G) \times \xi \left( \frac{L_C}{L_S}, D_C L_S \right)$$

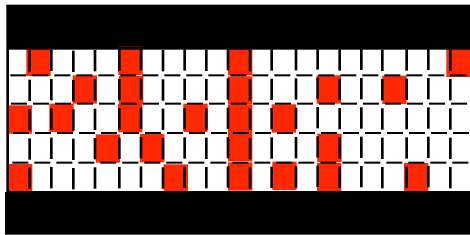
$$= A(V_G - V_{th}) V_D \times \xi \left( \frac{L_C}{L_S}, D_C L_S \right)$$

$$G = \frac{I_D}{V_D} \propto \xi \left( \frac{L_S}{L_C}, D L_S^2 \right) \approx \frac{1}{L_S} \left( \frac{L_S}{L_C} \right)^m (D L_S^2)$$

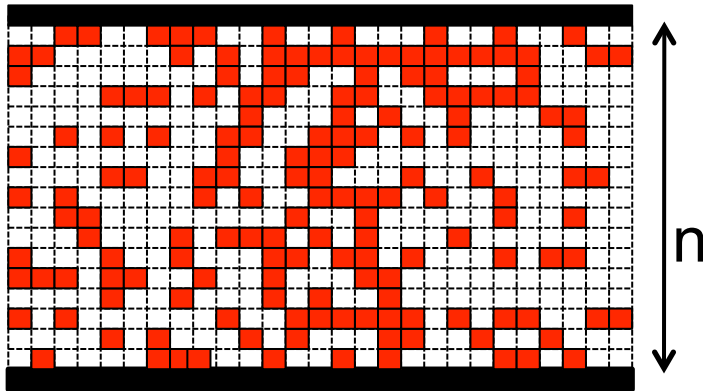
Do you see ohm's law at high D?

# lecture 3 vs. lecture 4

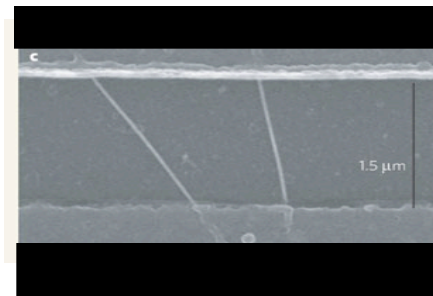
$$G \sim \sigma_{row} p^L \frac{W}{L}$$



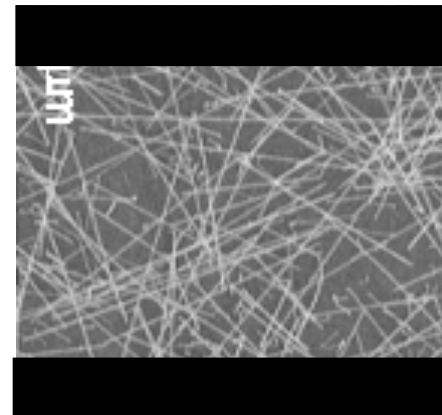
$$G \sim \sigma_{row} \frac{W}{L^\alpha}$$



$$G = \frac{2q^2}{\pi^2 \hbar} D_C L_S \left[ \sqrt{1 - \left( \frac{L_C}{L_S} \right)^2} - \frac{L_C}{L_S} \cos^{-1} \frac{L_C}{L_S} \right]$$



$$G \sim \frac{1}{L_S} \left( \frac{L_S}{L_C} \right)^{m(DL_S^2)}$$

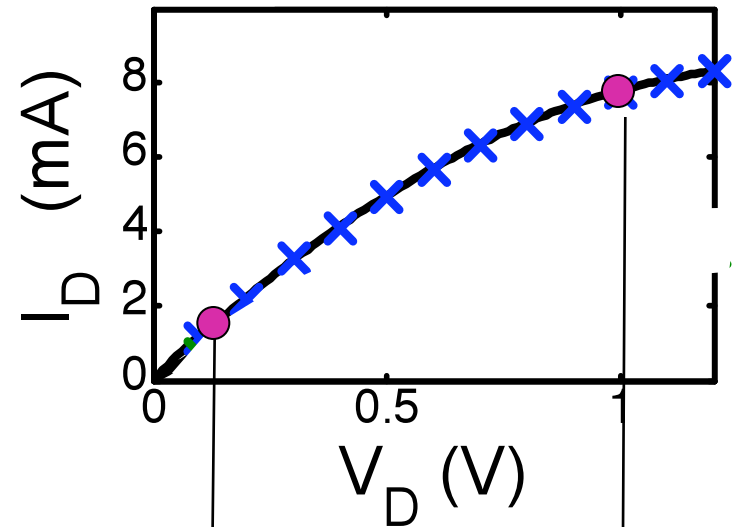
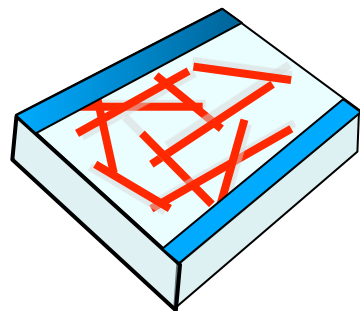


## outline lecture 4

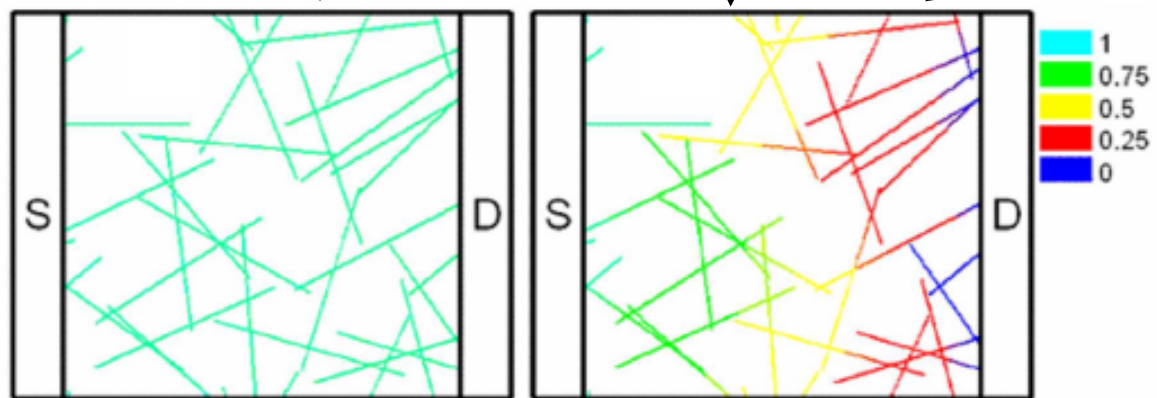
- 1) Stick percolation and nanonet transistors
- 2) Short channel nanonet transistors
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- 4) Transistors at high voltages**
- 5) Conclusions



# nonlinear I-V of nanonet transistors



Pinch off



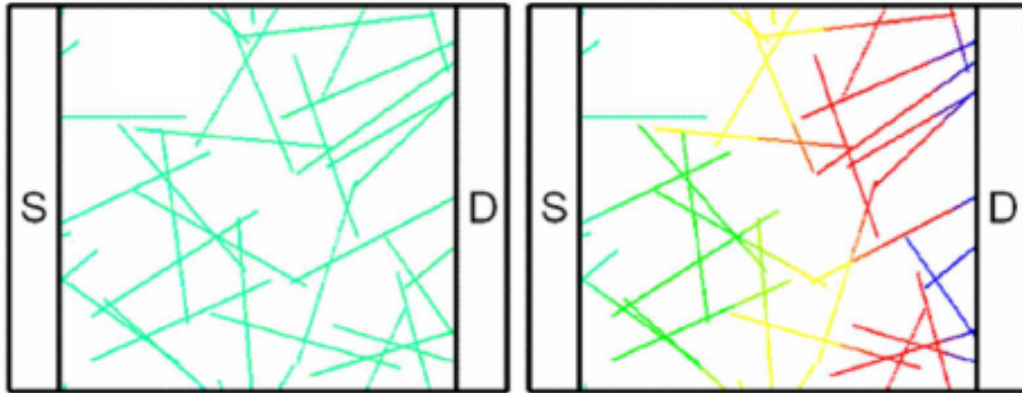
Low Bias

High Bias

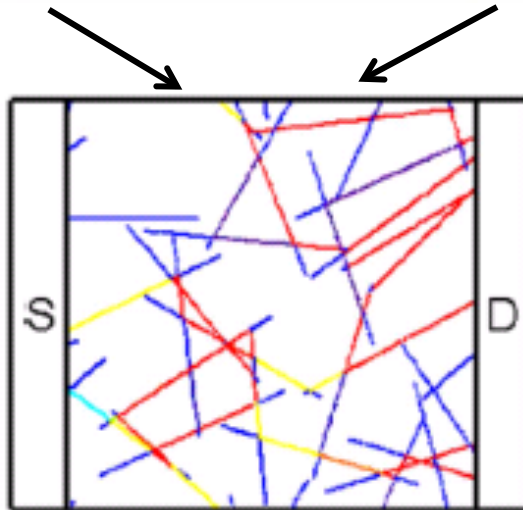
# voltage scaling in theory ...

Low Bias

High Bias



$$I_D = f(V_D, V_G) \times \xi \left( \frac{L_C}{L_S}, DL_S^2 \right)$$



$$\frac{I_{DL}}{f(V_{DL}, V_G)} = \xi \left( \frac{L_C}{L_S}, DL_S^2 \right) = \frac{I_{DH}}{f(V_{DH}, V_G)}$$

... scales exactly, as anticipated from short channel formula

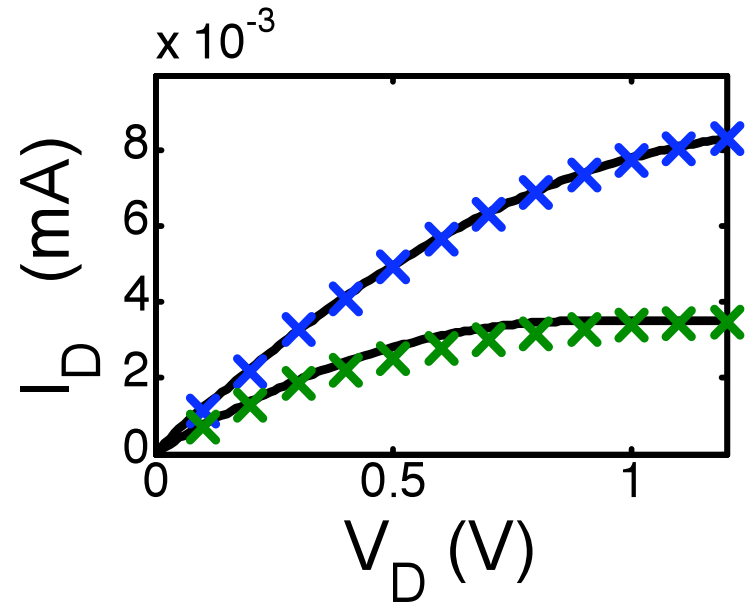
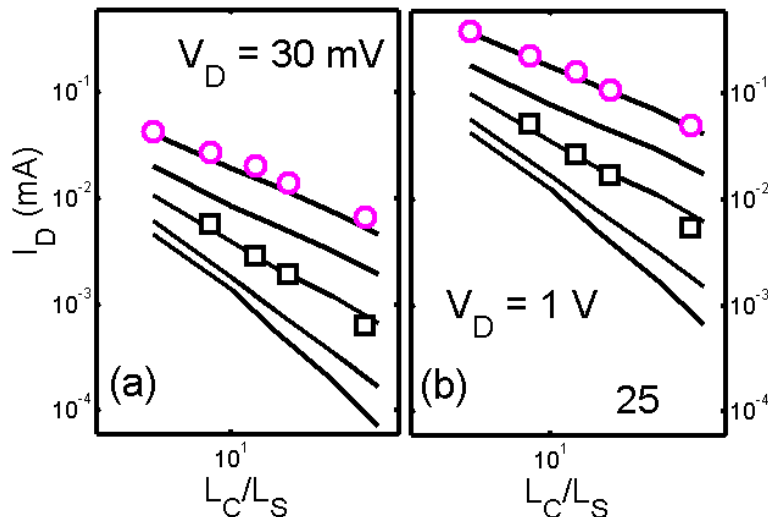
# ... and in practice!

$$I_D = \xi \left( \frac{L_S}{L_C}, DL_S^2 \right) f(V_D, V_G)$$

Hur et al. JACS, 2005  
Pimparkar et al. EDL, 2007

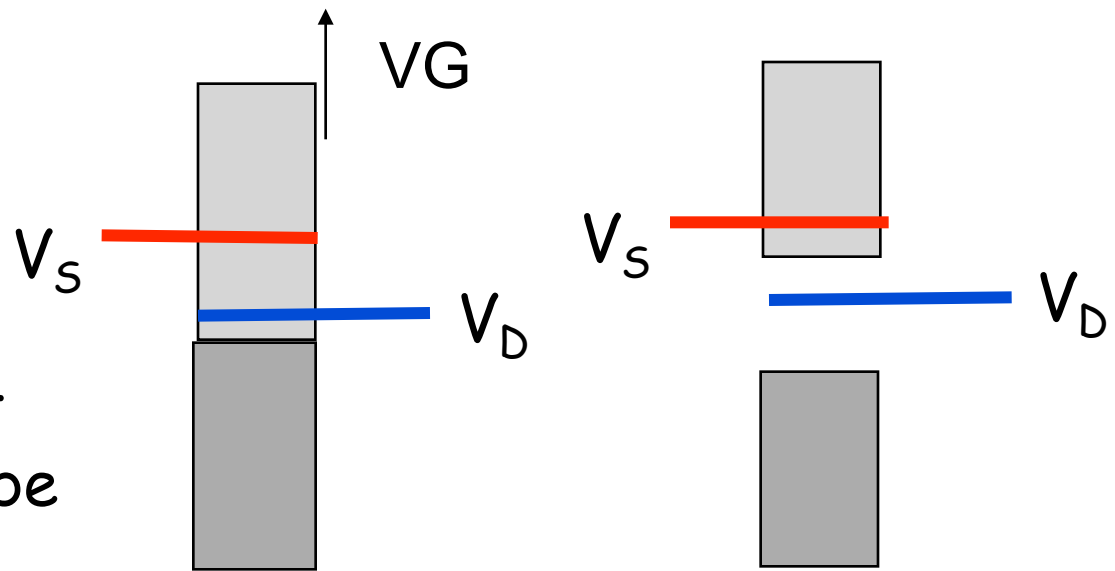
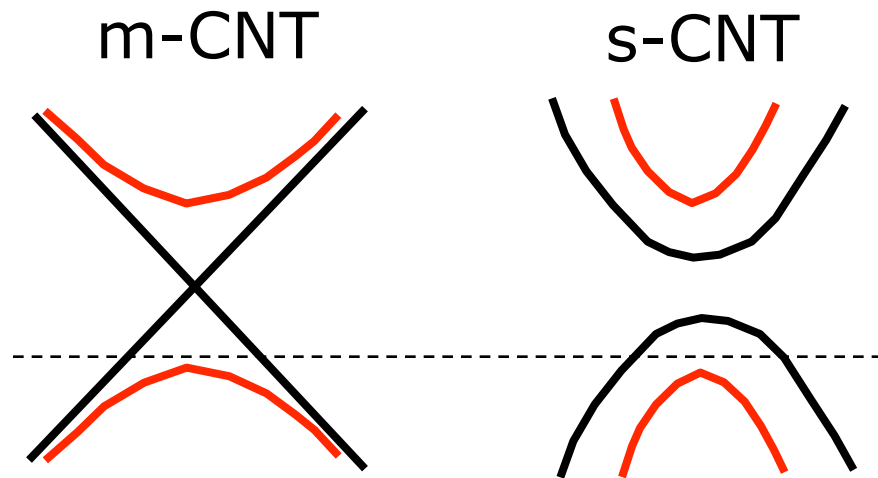
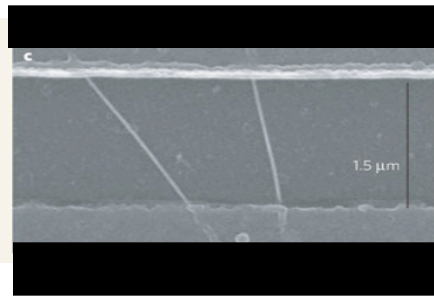
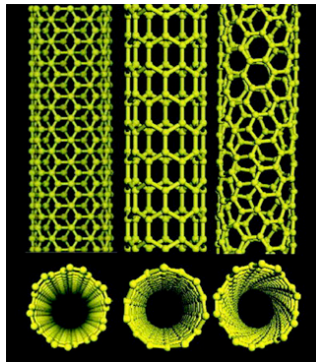
$$\xi \left( \frac{L_S}{L_C}, DL_S^2 \right) \approx \left( \frac{L_S}{L_C} \right)^{m(DL_S^2)}$$

$$f(V_G, V_D) = [(V_G - V_{TH})V_D - \beta V_D^2]$$



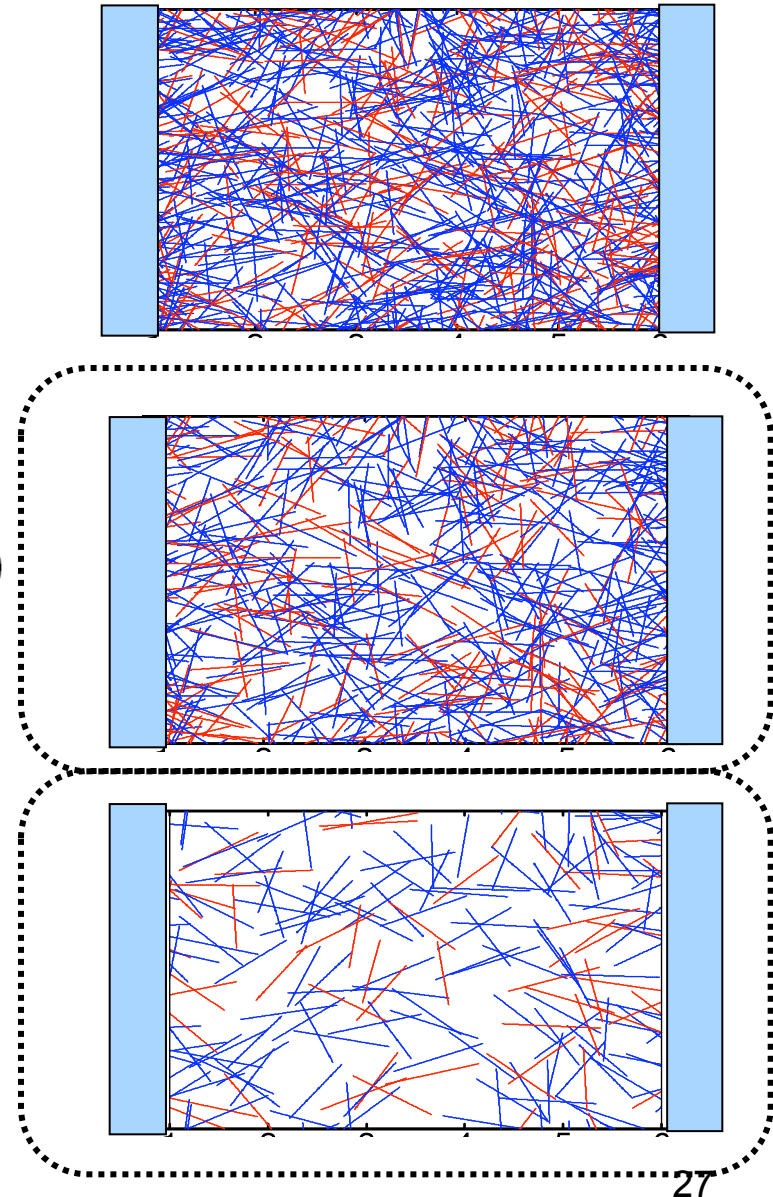
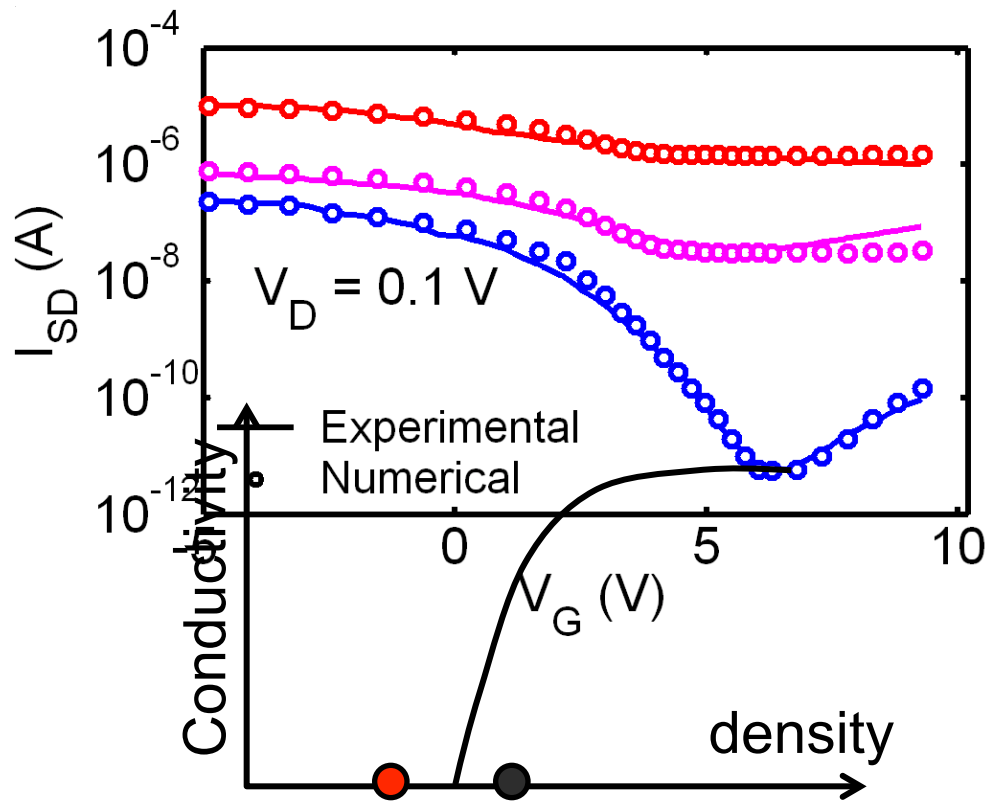
characterizing one transistor is sufficient ...

# metallic and semiconducting CNTs

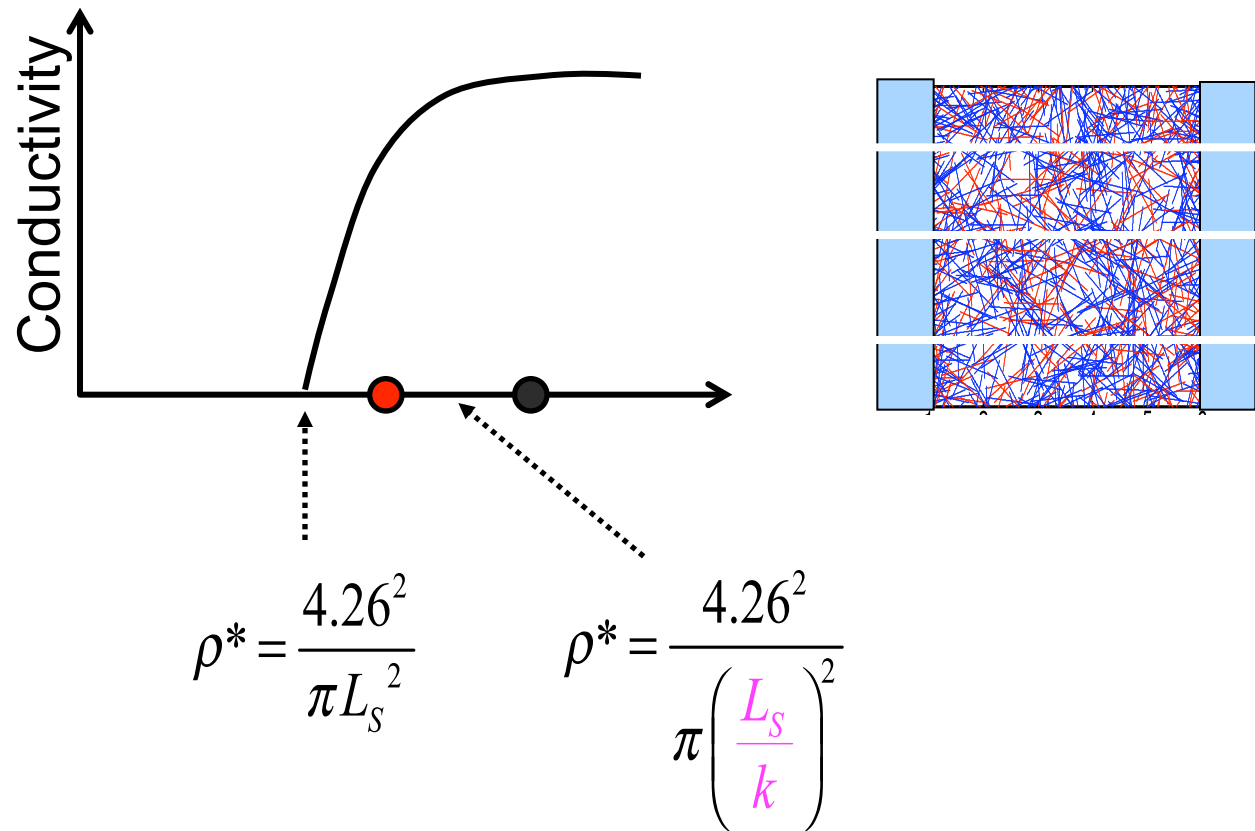


\* Metallic CNTs short transistors and must be eliminated.

# M-CNT Content: heterogeneous percolation



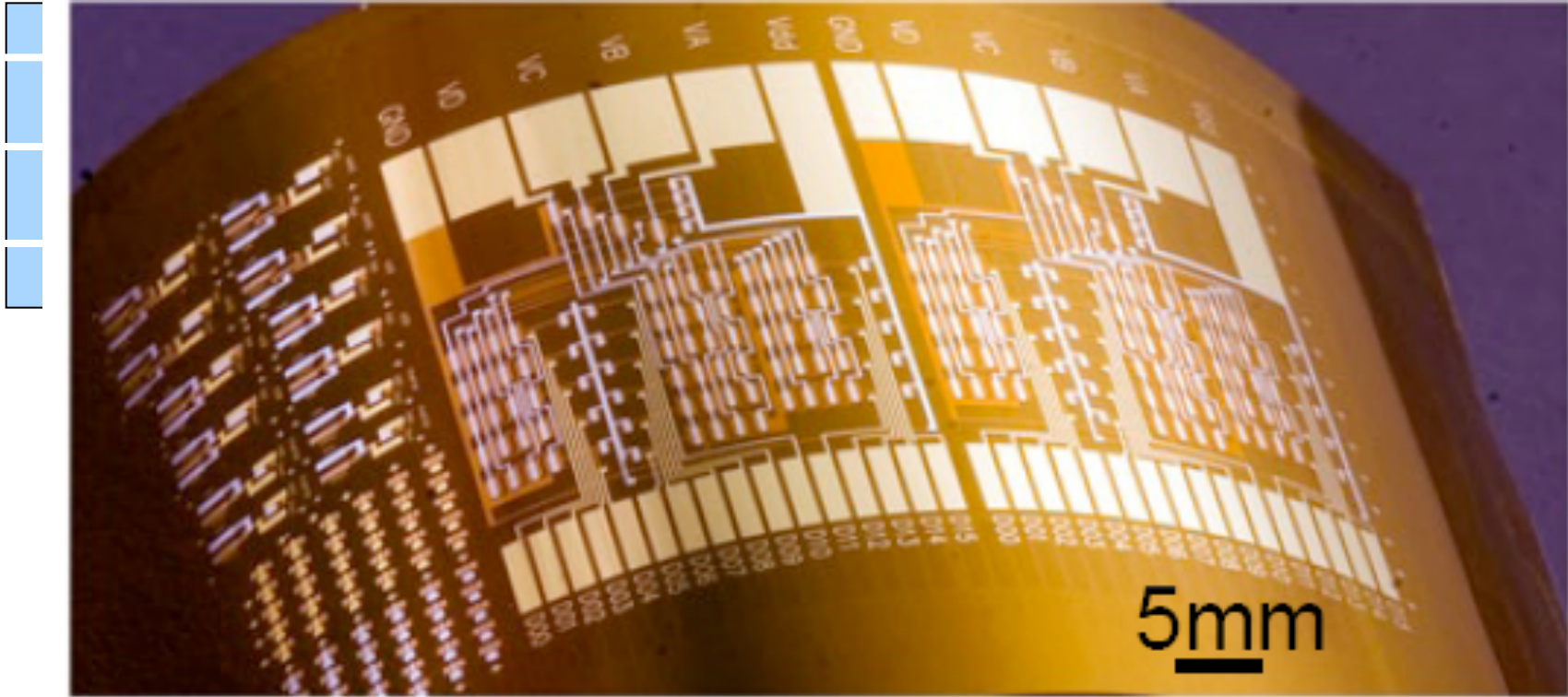
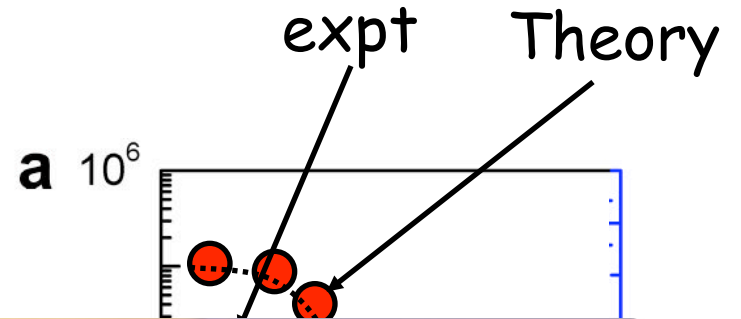
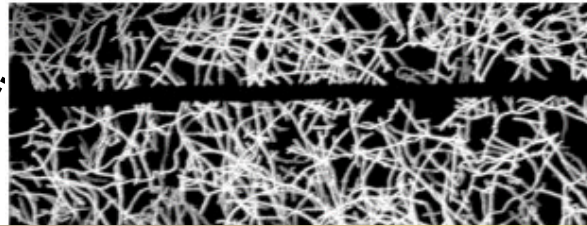
# 'striping' control of percolation threshold



Depending on the ckt, shorten the tubes by a factor  $k$ !  
 (\*see lecture 3 for the theory of  $p_c$  shift)



# Striping for improved on-off ratio



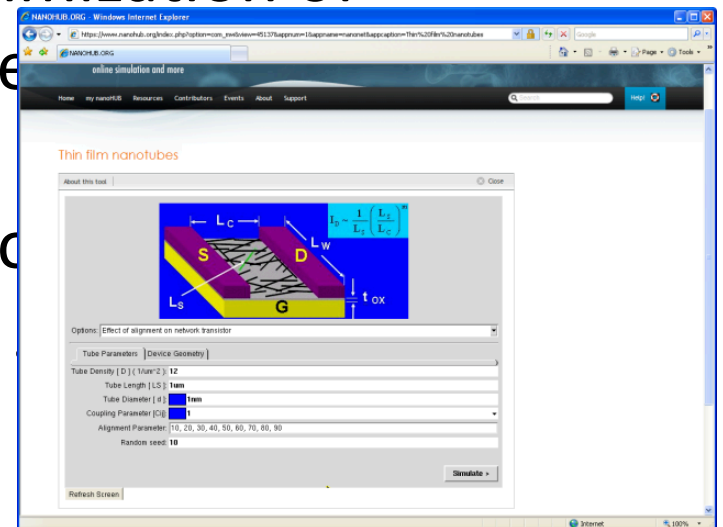


# conclusions

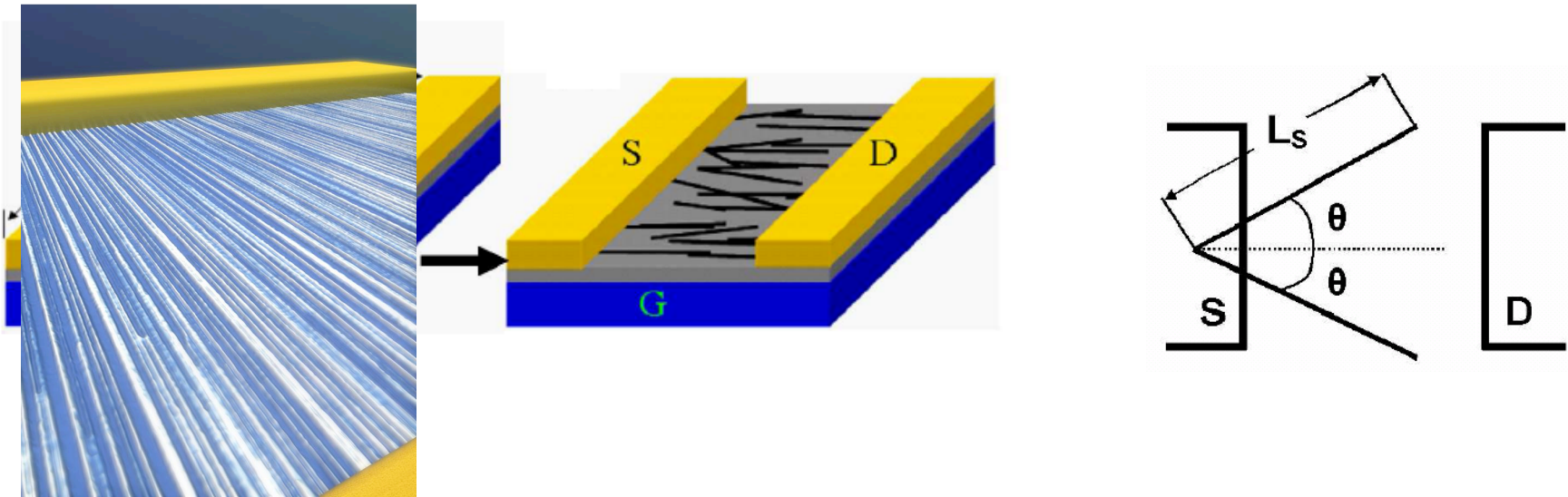
◆ Illustrated the power of stick percolation model by analyzing results of short and long-channel nanonet transistors.

◆ Nanonet transistors have become a testbed for the new theory of “Nonlinear percolation”. This new theory helped design of experiments, optimization of transistors, interpretation of device

◆ You will get additional insight into you do the HW using the Nanonet nanohub.org



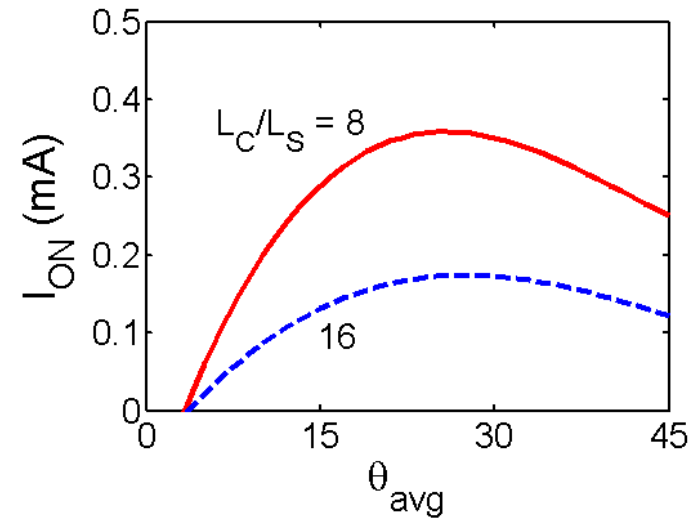
# alignment and asymmetric percolation



With more alignment

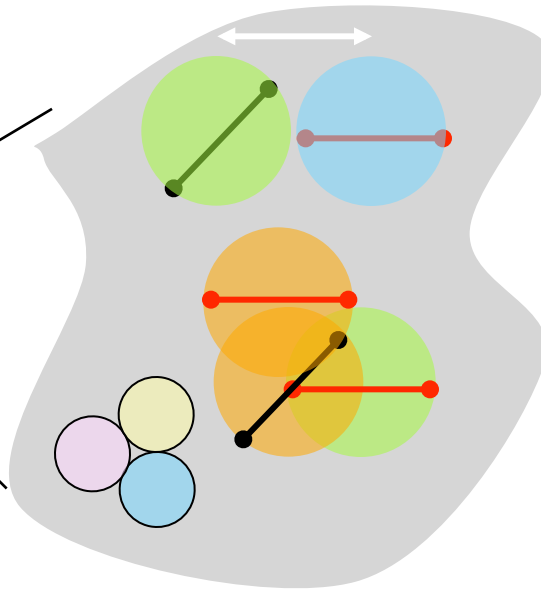
Av. Path length  $\downarrow \rightarrow I_D \uparrow$   
 No. of paths  $\downarrow \rightarrow I_D \downarrow$

Trade-off for optimal alignment



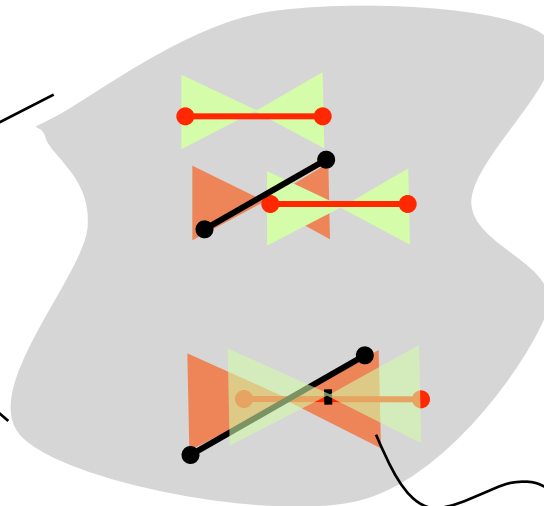
# percolation of quasi-aligned sticks

Random orientation



$$N_{C,R} \approx \frac{4}{\pi (L_S / 2)^2}$$

Quasi-aligned



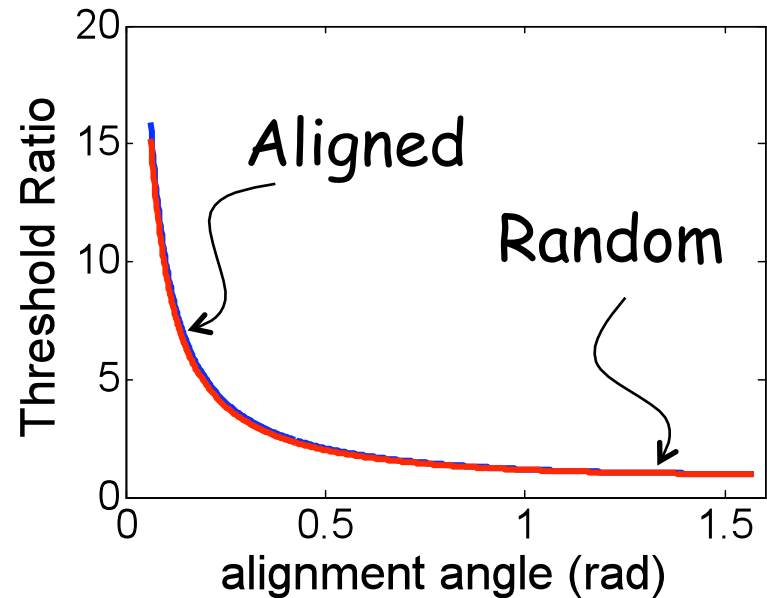
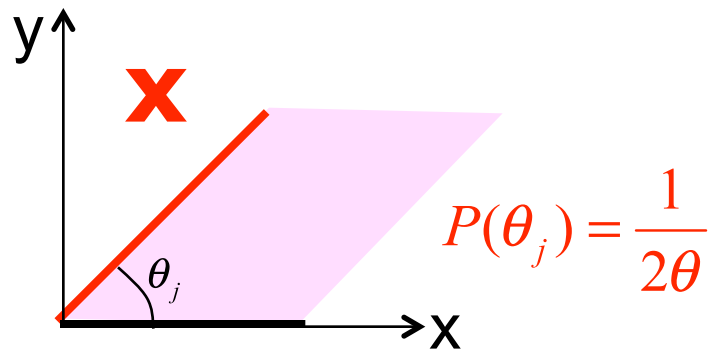
Fine for small  $q$

$$N_{C,\theta} \approx \frac{4}{\pi (L_S \sin(\theta) / 2)^2}$$

$L_S \sin(\theta) / 2$

# excluded volume for aligned stick . . . .

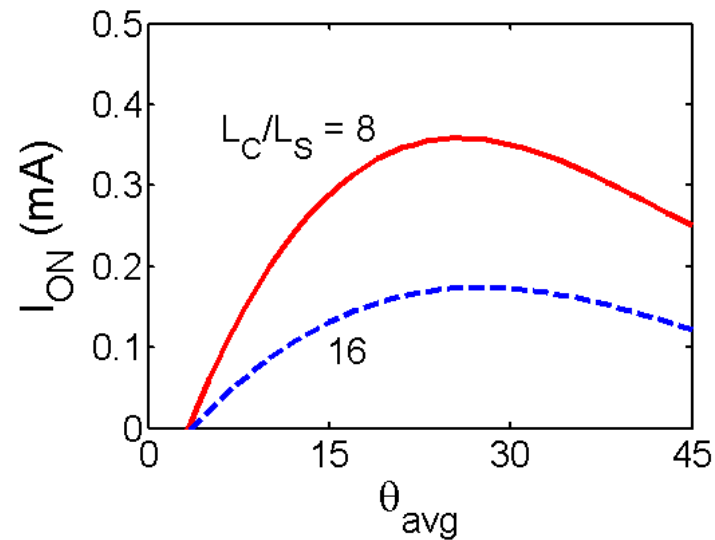
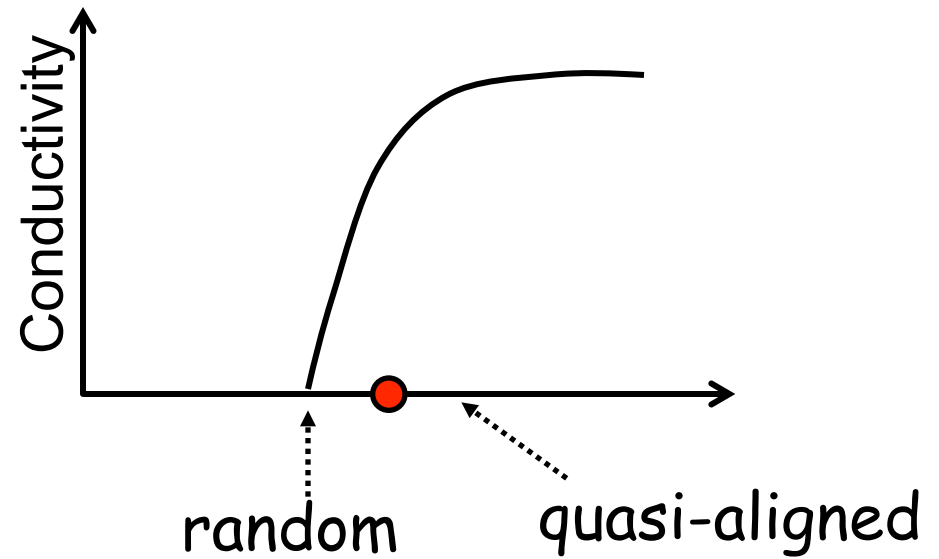
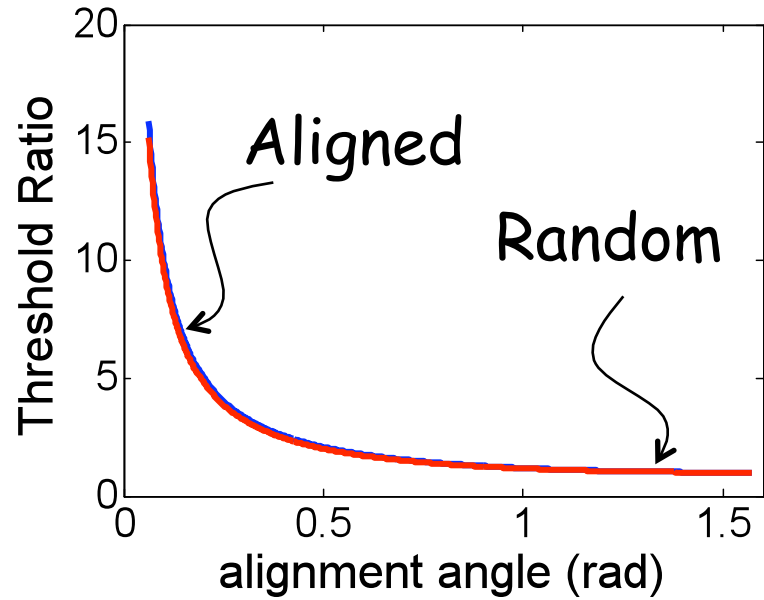
$$A_{\theta_i, \theta_j} = L_S L_S \sin(\theta_i - \theta_j)$$



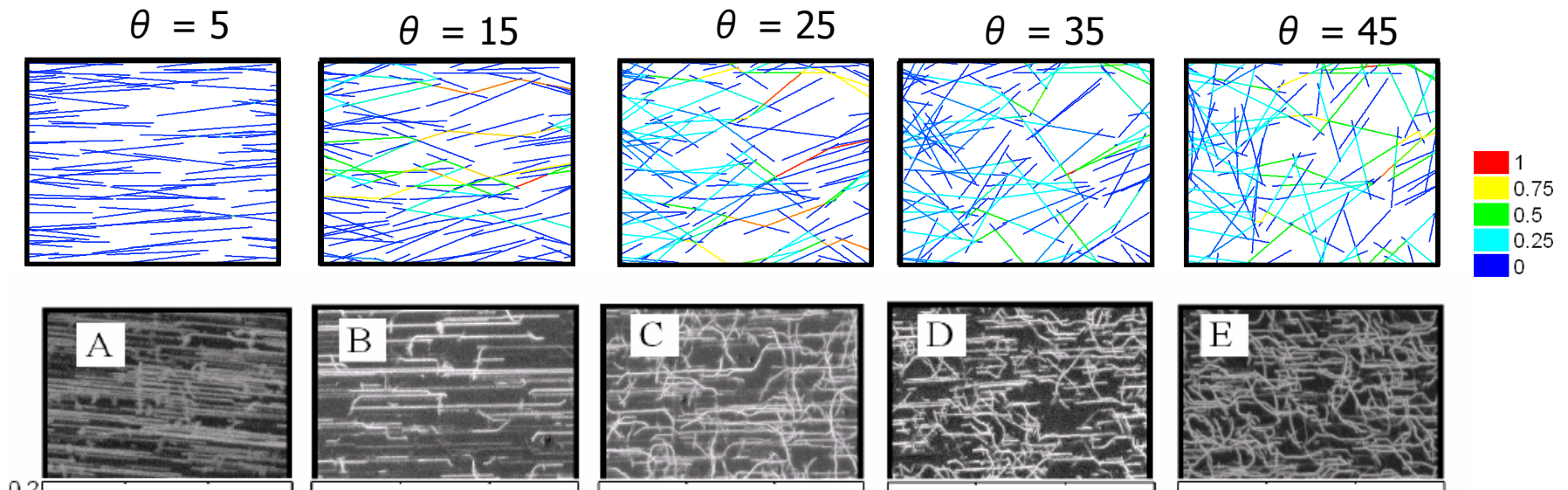
$$\begin{aligned} \langle A_{ex} \rangle_{\theta} &= \int_{-\theta}^{\theta} d\theta_i P(\theta_i) \int_{-\theta}^{\theta} d\theta_j P(\theta_j) \times A_{\theta_i, \theta_j} \\ &= \frac{L_S^2}{4\theta^2} [4\theta - 2\sin(2\theta)] \sim \frac{2L_S^2}{\pi} \sin \theta \end{aligned}$$

$$\begin{aligned} \langle A_{ex} \rangle_{\theta} N_{c, \theta} &\sim 1.8 \sim \langle A_{ex} \rangle_R N_{c, R} \\ \frac{N_{c, \theta}}{N_{c, R}} &\sim \frac{1}{\sin(\theta)} \end{aligned}$$

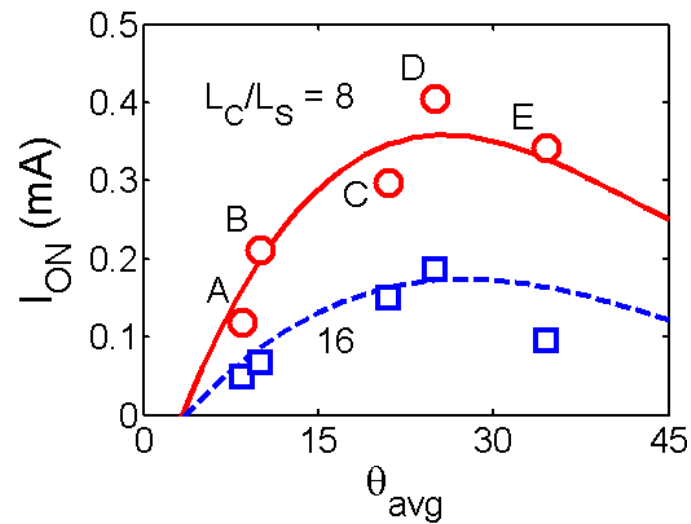
# alignment shifts of percolation threshold



# experimental verification



Pimparkar et al. MRS Spring meeting 2007



# conclusions

◆ Illustrated the power of stick percolation model by analyzing results of short and long-channel nanonet transistors.

◆ Nanonet transistors have become a testbed for the new theory of “Nonlinear percolation”. This new theory helped design of experiments, optimization of transistors, interpretation of device

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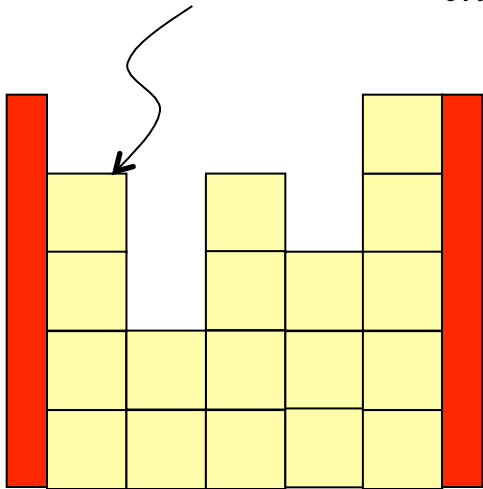


# Appendix



# scaling relationship: simple argument

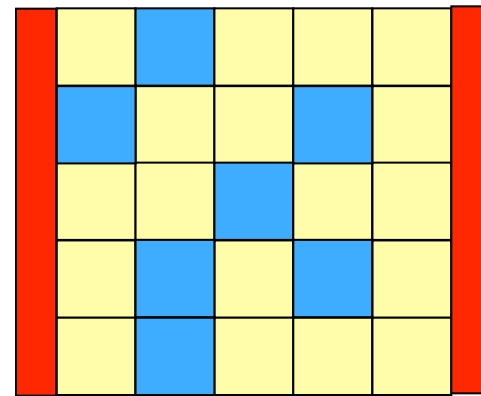
$$I_D(x) = W(x)qn\mu \frac{dV}{dx}$$



$$\int_0^{L_C} \frac{I dx}{W(x)} =$$

$$\int_0^{V_D} C_{ox} (V_G - V_T) \mu dV$$

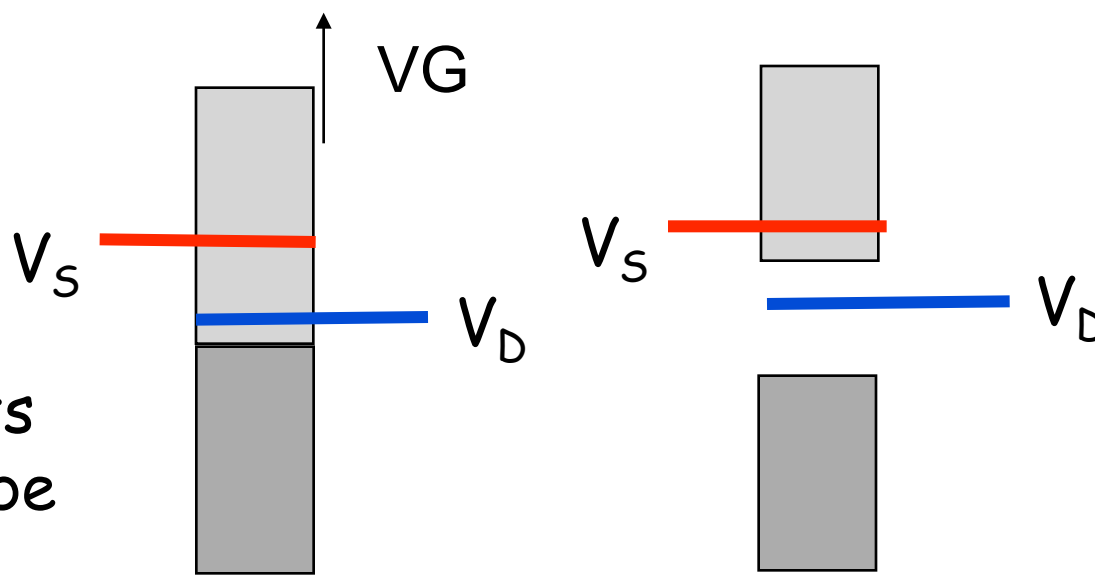
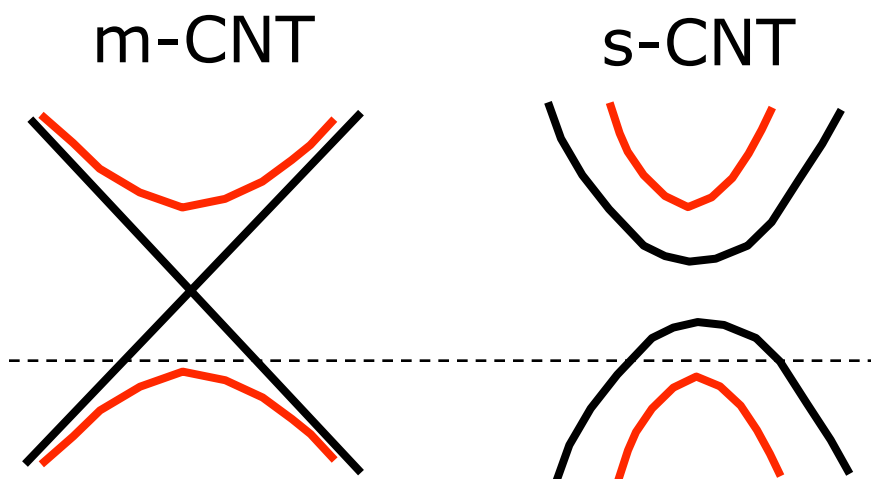
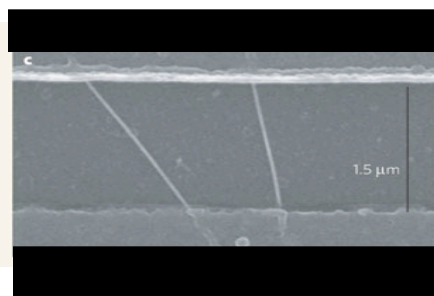
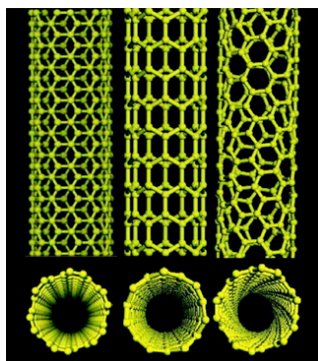
$$I_D = \frac{A}{L_S} \xi \left( \frac{L_S}{L_C}, DL_S^2 \right) \times f(V_G, V_D)$$



$$I = \frac{\mu C_{ox}}{\int_0^{L_C} W(x)^{-1} dx} \left[ (V_G - V_T) V_D - V_D^2 / 2 \right]$$

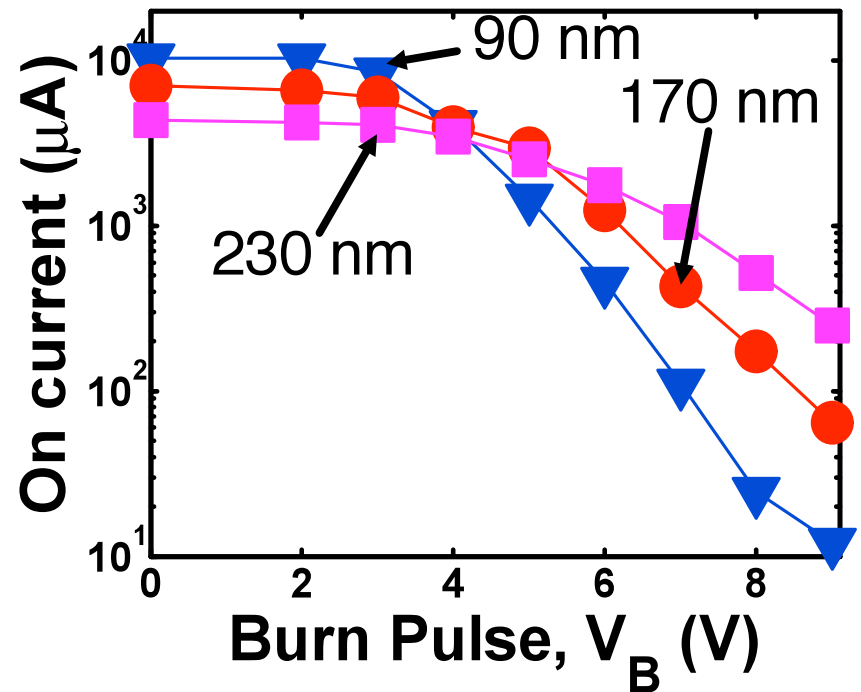
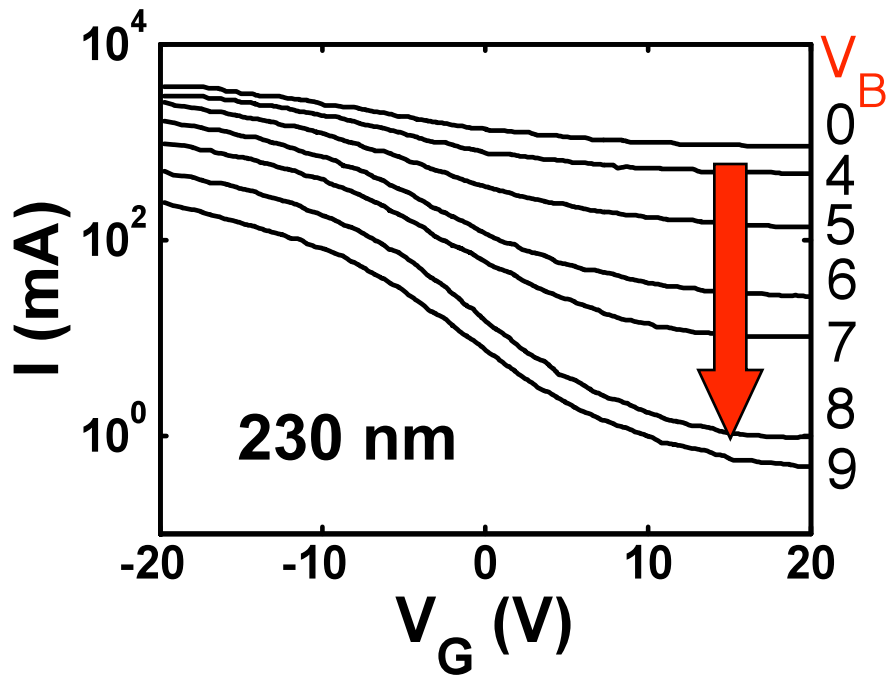
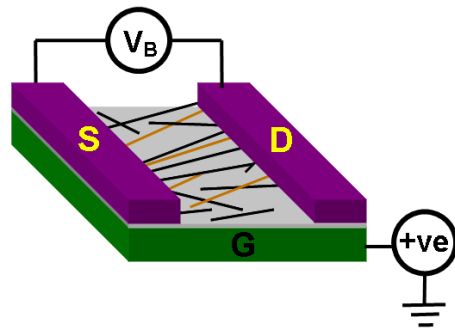
$$= \frac{A}{L_C} \xi(p_i, L_C) \times f(V_G, V_D)$$

# metallic and semiconducting CNTs

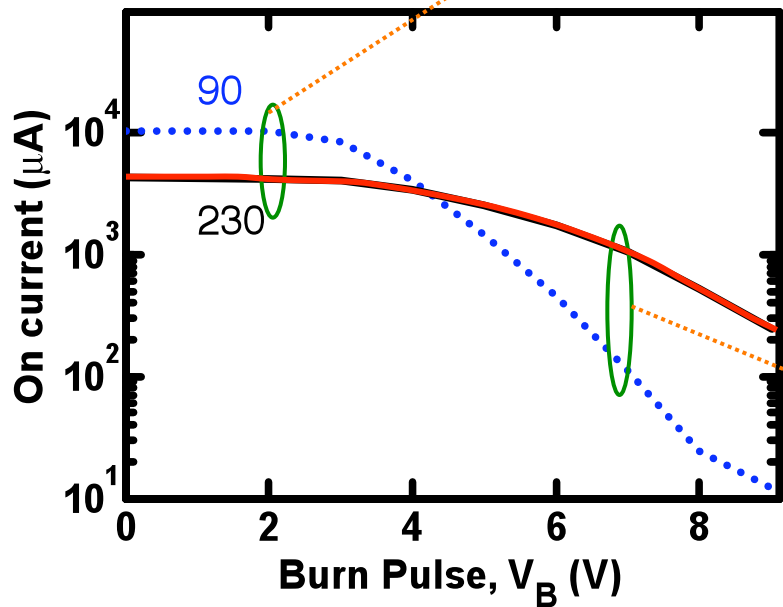


\* Metallic CNTs shorts transistors and must be eliminated.

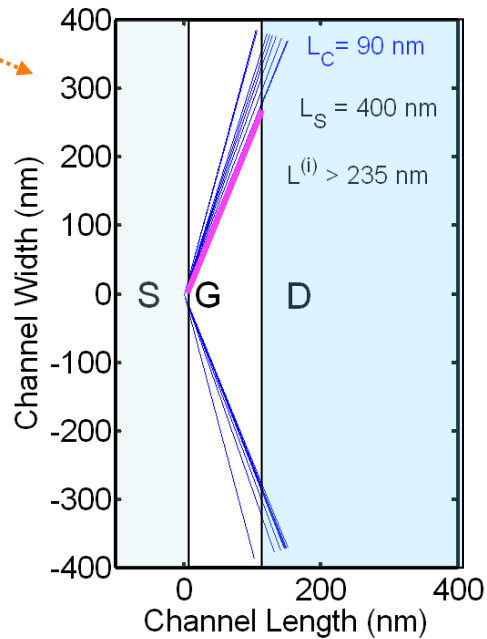
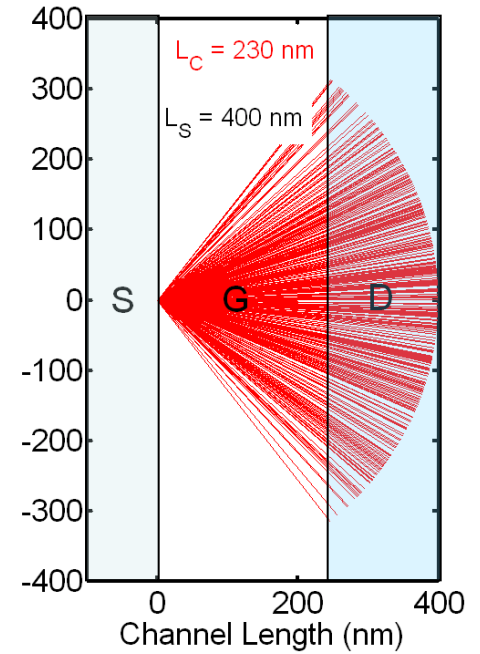
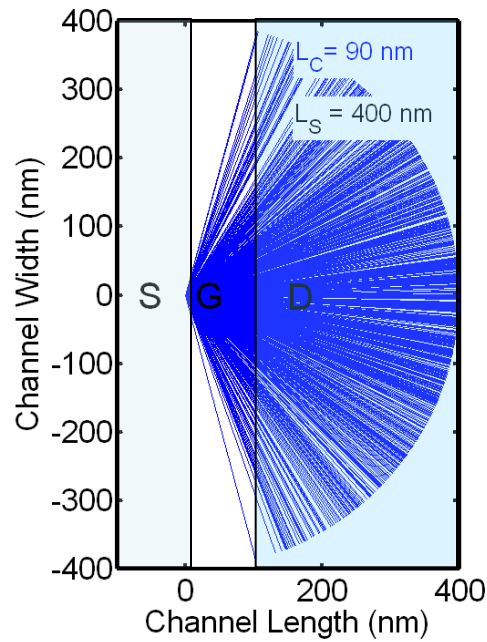
# electrical burning of metallic nanotubes



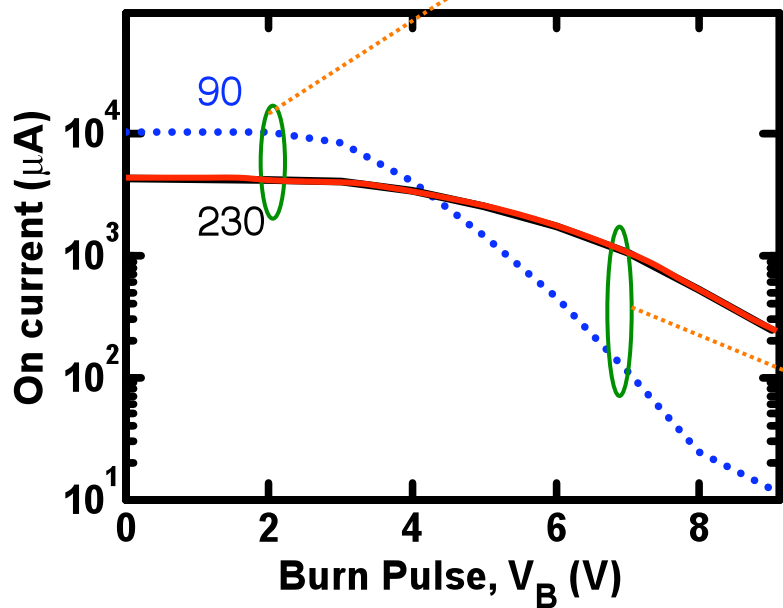
# Interpretation ..



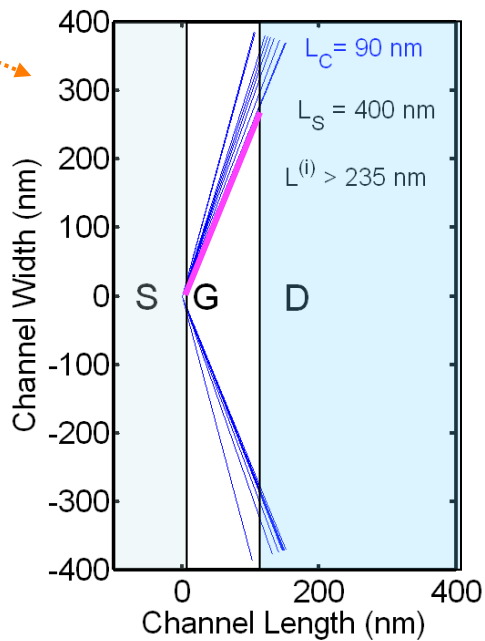
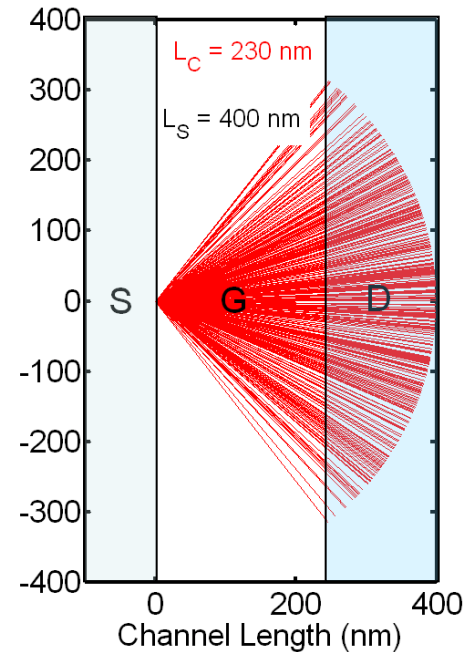
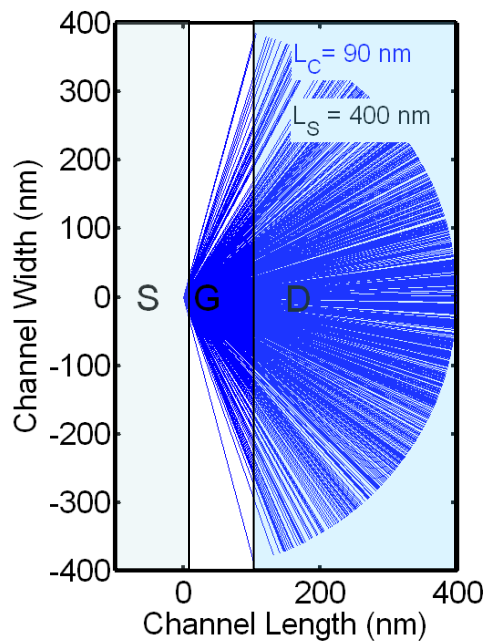
$$L_B = \frac{V_B}{I_{CRIT}}$$



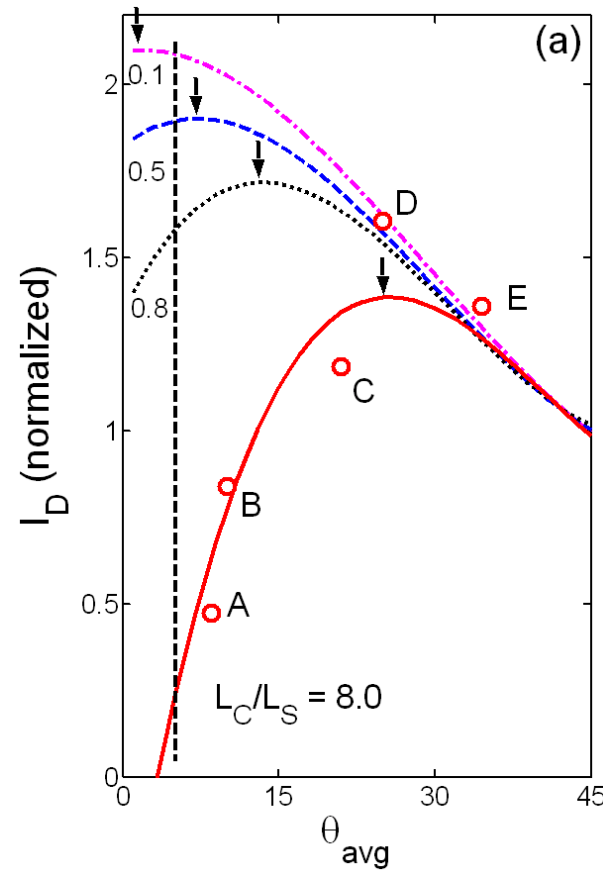
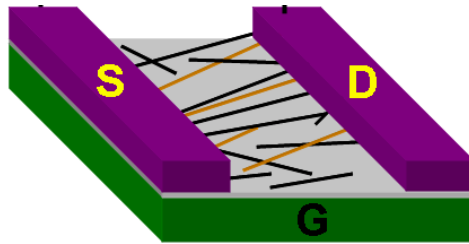
# interpretation ...



$$L_B = \frac{V_B}{I_{CRIT}}$$



# alignment of short nanonet transistors...



Random network is close to optimal !