Checklist for submitting a compact model to NEEDS:

To publish a compact model on needs.nanoHUB.org, be sure that your Verilog-A script follows the guidelines at https://nanohub.org/resources/18621.

Create a private project on nanoHUB (https://nanohub.org/projects/) to upload your files and collaborate with other authors of your Compact Model.

(Examples of each of these files can be found at https://nanohub.org/publications/15)

- ☐ Verilog-A file(s): modelname_version.va*

- ☐ Circuit simulation benchmark files: modelname_version_verilog_test_bench.zip

- ☐ Matlab exerciser: modelname_version_exerciser.zip (optional)

- ☐ Model parameter extractor (optional) or parameter sets (required if extractor not provided): modelname_version_parameter_extractor_Matlab.zip or modelname_version_parameters.zip

- ☐ Manual: Modelname_version_manual.pdf

- ☐ Example device data files: modelname_version_experimental_data.zip

*where "modelname" is the short name of the model to be used in all files
** where "version" in the filename is in the form of x_y_z and x, y, and z conform to the versioning standards outlined by the CMC.

When you are ready to submit your model, choose the "Publication" option from the left-hand menu in Projects. You will also need to provide the following information

- ☐ Model Name
- ☐ Version number (follow Compact Model Council Versioning conventions: https://nanohub.org/groups/needs/users_developers)
- ☐ Short Description (shows up in search results)
- ☐ Abstract
- ☐ Key References
- ☐ Authors and Affiliations
- ☐ Details of changes after first version
- ☐ Information for LICENSE (Organization, Year, name(s) of person(s) providing the license (usually one or more authors), and date of license)
- ☐ Keywords